GLLM317L

100mA ADJUSTABLE VOLTAGE REGULATOR

Description

The GLLM317L is a monolithic integral circuit, designed for use as positive adjustable voltage regulator. It is designed to supply unit 100mA of load current with an output voltage adjustable over a 1.25V to 37V range.

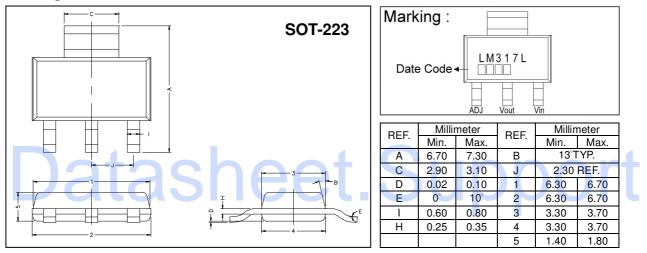
Features

• Output voltage adjustable from 1.25V to 37V

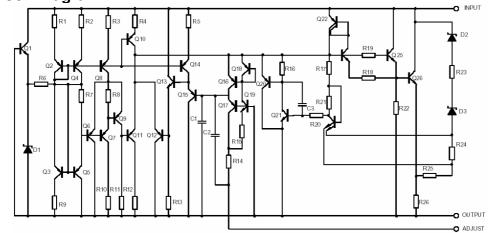
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- Output current in excess of 100mA
- Thermal overload protection
- Short circuit protection
- Output transistor save area compensation
- Floating operation for high voltage applications

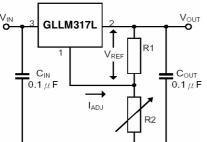
Package Dimensions



Block Diagram



Test Circuit



Absolute Maximum Ratings at Ta = 25℃

Parameter	Symbol	Ratings	Unit
Input-Output Voltage Difference	VIN - VOUT	40	V
Power Dissipation	PD	1	W
Junction Temperature	TJ	+125	°C
Operating Junction Temperature	Topr	0 ~ +70	°C
Storage Temperature Range	Tstr	-40 ~ +150	°C

Note 1. Absolute maximum ratings are those values beyond witch the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The device is guaranteed to meet performance specification within 0° C ~ 70° C operating temperature range and assured by design from -20°C ~ 85 °C.

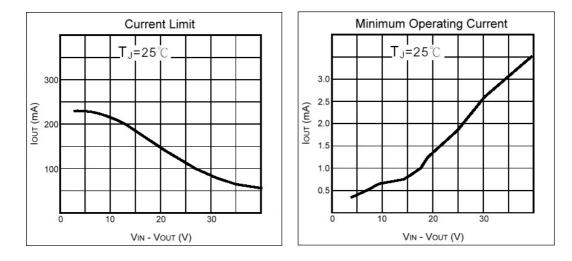
Electrical Characteristics

(VIN - VOUT=5V, $0^{\circ}C \le T_J \le 125^{\circ}C$, IO=40mA, unless otherwise specified)

Parameter	Symbol	Test Cor	Min	ТҮР	Max	Unit	
Line Regulation	REGLINE	Vin -Vout=3∼40V Io<20mA	T J =25 ℃	-	0.01	0.04	%/V
	REGLINE		0°C ≤ TJ ≤ 125°C	-	0.02	0.07	%/V
Load Regulation		Io=5~100mA Vouт≤ 5V	TJ=25℃	-	5	25	mV
	REGLOAD		0°C ≤ TJ ≤ 125°C	-	20	70	
	REGLOAD	Io=5~100mA Vou⊤≥ 5V	TJ=25℃	-	0.1	0.5	%/Vout
			0°C ≤ TJ ≤ 125°C	-	0.3	1.5	
Adjustable Pin Current	ladj			-	50	100	μA
Adjustable Pin Current Change	ΔIadj	Viℕ -Vout=3~40V Io=5~100mA, PD < 625mW		-	0.2	5	μA
Reference Voltage	VREF	Viℕ - Vout=3~40V Io=5~100mA, Pd < 625mW		1.2	1.25	1.3	V
Output Voltage Temperature Stability	ΔVουτ/Vout			-	0.7	-	%
Minimum Load Current	ILOAD(Min)	VIN -VOUT=40V		-	3.5	5	mA
Maximum Output Current		VIN -VOUT=3~13V		100	200	-	mA
	lout(Max)	VIN -VOUT=40V		25	50		
Output Noise Voltage (% of Vour)	eN	f=10Hz ~ 10KHz, TJ=25℃		-	0.003	-	%/Vout
Ripple Rejection	RR	TJ=25℃, f=120Hz	Cadj=0	-	65	-	dB
			CADJ=10uF	66	80	-	uв

Note: CADJ is connected between Adjust pin and Ground.

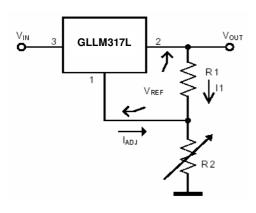
Characteristics Curve



Application Information and Circuits

The GLLM317L provides an internal reference voltage of 1.25V between the output and adjustments terminals. This is used to set a constant current flow across an external resistor driver, giving an output voltage VouT of:

VOUT = VREF * (1 + R2 / R1) + IADJ * R2 The device is designed to minimize the term IADJ (100uA max) and to maintain it very constant with line and load charges. Usually, the error term IADJ*R2 can be neglected. To obtain the previous requirement, all the regulator quiescent current is returned to the output terminal, imposing a minimum load current condition. If the load is insufficient, the output voltage will rise. The GLLM317L is a floating regulator, input-output differential voltage, supplies of very high voltage with respect to ground can be regulated as long as the maximum input-output differential is not exceeded. Furthermore, programmable regulators are easily obtainable and, by connecting a fixed resistor between the adjustment and output, the device can be used as a precision current regulator.





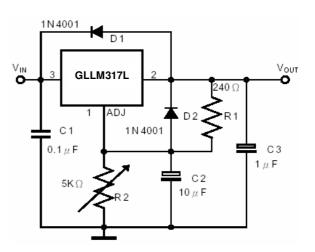


Fig 2. Voltage Regulator with Protection Diodes

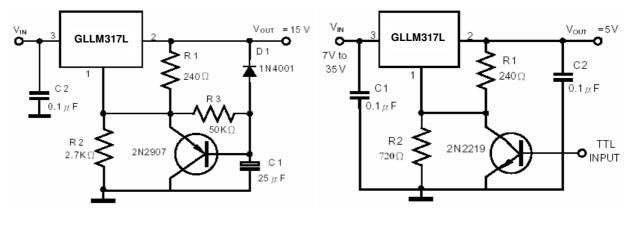


Fig 3. Slow Turn-On 15V Regulator



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