For detailed explanations of			$\Box$							П	]							EUROPE	USA	
column heading notations, see	CASE	LD	ΓD	LD	LD	LD	LD	LD	LD	rD	LD	LD	LD	LD	LD	LD	LD	SUBSTI-	SUBSTI-	S TYPE
Арр. А.	(APP F)	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	TUTE	TUTE	SNUMBER
Also for ready references the	TO5 - 0 /1M	N	E-	E+	V-M	N ·	R	V+	N									SFC2207	LM207H	0 AMLM207H
more important abbreviations	T05-8/1M	1	F			E+		V-	N	N.	R	٧+	F*	N.	N.			UA208AD	LM208AD	O AMLM208AD
used in the column headings are	DIL-14/10		1 '	N	E-		N V-			F*	F	**	F.	N ·	N	•	•	UAZUGAD	LM208AF	0 AMLM208AF
listed below:	FLP-10/30		N	E-	E+	N		R	V+ F*	1,	-	•	•			•		SFC2208A	į.	O AMLM208AH
LEFT HAND PAGE	T05-8/1M	F	E-	E+	∨-м	N	R	٧+		. ·	٠.	v	-:	ļ., ·					LM208AH	
APP = application	DIL-14/10	N	F	N	E-	E+	N	V-	N	N [	R	۷+ (	F*	N	N			UA208D	LM208D	O AMLM208D
(codes at APP.E.)		١	١	_		١	١.,	_		1	_							CEGGGGGGT		NA 11200E
CMRR = common mode	FLP-10/30		N	E-	E+	N	٧-	R	٧+	F*	F	٠.				•		SFC2208PT	LM208F	0 AMLM208F
	T05-8/1M	F	E-	E+	V-M	N	R	٧+	F*	l. · l			-:	l :		•		SFC2208	LM208H	0 AMLM208H
rejection ratio	DIL-14/10		N	Τ	N	E+	٧-	N	N	L.	R	۷+	T*	N	N			SN52110JA	LM210D	0 AMLM210D
CMP = compensation	FLP-10/30	N	T	N	E+	٧-	L	R	<b>V</b> +	T*	N	٠.					•		LM210F	0 AMLM210F
(frequency)	T05-8/1M	ĮΤ	N	E+	٧-	L	R	٧+	T*	J • J				ļ · ,				MLM210G	LM210H	0 AMLM210H
$dV_{i0}/dT = input offset voltage$				l _	_			l_		_		l., i	l	i						
temperature drift	DIL-14/10		G	E+	E-	N	٧-	Ţ	T*S	R	N	٧+	N	N	N			SN52111J	LM211D	0 AMLM211D
GBP = gain bandwidth	T05-8/1M	G	E+	E-	٧-	Т	T*S	R	۷+	-								SFC2211	LM211H	0 AMLM211H
product	DIL-14/1F	N	T	₩	E-	E+	₩*	٧-	N	F	R	٧÷	T*	N .	N				LM212D	0 AMLM212D
I <sub>B</sub> = input bias current	T05-8/1M	T	E-	E+	٧-	F	R	٧+	T*						•				LM212H	0 AMLM212H
in = input bias offset	FLP-10/30	N	W	E-	E+	₩*	٧-	R	٧+	Τ .	T*	٠.			.				LM212F	0 AM212-FLP
current		]	ļ	ļ	J	1	ļ	ļ						J.				J	J	] ]
	DIL-14/10		T	₩	E-	E+	W*	۷-	N	F	R	٧+	T*	N	N			MLM216AD	LM216AD	0 AMLM216AD
l <sub>0</sub> = quiescent supply	FLP-10/30	N	W	E-	E+	₩*	٧-	R	٧+	T	T*								LM216AF	0 AMLM216AF
current	T05-8/1M	T	E-	E+	٧-	F	R	٧+	T*			,							LM216AH	0 AMLM216AH
MFR = manufacturer	DIL-14/10	N	T	₩	E-	E+	₩*	٧-	N	F	R	۷+	T*	N	N				LM216D	0 AMLM216D
(codes at App.C.)	FLP-10/30		w	E-	E+	W*	٧-	R	۷+	T	T*				.				LM216F	0 AMLM216F
$P_0$ = quiescent power		ļ				_			i											11
consumer	T05-8/1M	T	E-	E+	٧-	F	R	٧+	T*										LM216H	0 AMLM216H
PSRR = power supply rejection	DIL-14/10	N	N	T*F	E-	E+	¥-	N	N	F*1	R	V+	ø	N	N			SN52118JA	LM218D	O AMLM218D
ratio	FLP-10/30		T*F	E-	E+	V-	F*T	R	٧+	ø	N		Δ.						LM218F	0 AMLM218F
V <sub>ICM</sub> = common mode input	T05-8/1M	T*F		E+	٧-	F*T	R	V+	ø									TDB0118CM	LM218H	0 AMLM218H
voltage rating	DIL-14/10		R1	V+	1 '	E+1				F-3	E-4	E+4	G	R4	R3			TDE0119DP	LM219D	0 AMLM219D
$V_{\text{III}}$ = differential input		1							-				-			-				1
	FLP-10/30	R1	G1	E+1	E-1	V-	R2	G2	E+2	E-2	V+			١.			١.		LM219F	0 AMLM219F
voltage rating	T05-10/1N		GI	E+1	1		R2	G2	E+2				ļ .	Ι.	l '	Ü	١.	TDE0119CM	LM219H	0 AMLM219H
$V_{10}$ = input offset voltage	DIL-14/10			E+1		E+2	1	R2	R3	E-3		G	E+4	E-4	R4			DEGITION	LM224AD	O AMLM224AD
$V_{\rm S} = dc$ supply voltage	DIL-14/10		E-1				E-2		R3	E-3			E+4		R4		١.		LM224D	O AMLM224D
	DIL-14/10		N E-1	G1		E-1		R2	G2	E+2			R1	N	N	•	١.	MLM239AL	LM239AD	O AMLM239AD
RIGHT HAND PAGE	DIL 14/10	1"	"	91	1,1		'	^2	uz	- 4	۲ ۲	<b>'</b> '	\ \_	"	"			mLm2,33AL	LMZJJAU	O AMEMAS SAID
Lead out coding summary	DIL-14/10		R1	V+	E-1	EA1	E-2	E+3	E+3	E-3	E-4	E+4	G	R4	R3		ļ	MLM239L	LM239D	0 AMLM2390
(details at APP.G.) for different	DIL-14/10		E-1				E-2		R3	E-3			E+4		R4	-	١.	MLM2JJL	LM248D	0 AMLM248D
cases (APP.F.)	DIL-14/16			E+1			E-2		R3	E-3				E-4		•			LM249D	0 AMLM249D
A = gain adjust	DIL-14/1F		E-	E+	V-	T*	R	V+	F*	15-3	E 13	, ,	E 74		77	•	·	TDA0301D	LM301AD	O AMLM301AD
B = bias adjust					, .	1 '			F*	-		٠.				•	١.	t .		1 [
	T05-8/1M	FT	E-	E+	V-M	T*	R	٧+	F*	٠.		٠.		•		•	١.	SFC2301AH	LM301AH	0 AMLM301AH
	DTI 0 (1D		_	١	1	٠.	_	١.,.	F*									CE02201 ADO		0
- · · · · · · · · · · · · · · · · · · ·	DIL-8/1P	FT	E-	E+	٧-	T*	R	۷+	1.	-:	<u> </u> `	u.:	-:	. ·			•	SFC2301ADC	l	0 AMLM301AN
	DIL-14/1F		N	FT	E-	E+	٧-	N	N	T*	R	٧+	F*	N	N				LM301D	0 AMLM301D
$F,F^* = input frequency$	FLP-10/30		FT	E-	E+	٧-	T*	R	٧+	F*	N	٠.		•		•	•		LM301F	0 AMLM301F
compensation	T05-8/1M	FT	E-	E+	V-M	T*	R	V+	F*	l -		١.		-			١.	SFC2301A	LM301H	0 AMLM301H
G = ground	DIL-14/10	N	N	Т	N	E+	٧-	N	N	L	R	٧+	T*	N	N				LM302D	0 AMLM302D
J = high level input			l	1	1		l_		_				1							
K = output, open collector	T05-8/1M	T	N	E+	٧-	L	R	٧+	T*		· '	١.		-	-			UA302C	LM302H	0 AMLM302H
L = output, open emitter	T05-8/1M	G	E+	E-	V-M	S1	S2	R	۷+		• '	١.	.	•	.		.	SN72306L	LM306H	O AMEM306H
M = metal case	DIL-14/1P		N	N	E-	E+	٧-	N	N	N	R	٧+	N	N	N			SN72307JA	LM307D	0 AMLM307D
N = not connected	T05-8/1M	N	E-	E+	V-M		R	٧+	N			١.		.				SFC2307	LM307H	0 AMLM307H
0 = special terminal	DIL-14/1F	N	F	N	E-	E+	N	٧-	N	N	R	¥+	F*	N	N			SN72308AJA	LM308AD	O AMLM308AD
*							l						1							
	T05-8/1M	F	E-	E+	V-M	N	R	٧+	F*		.	١.			١.,			SFC2308A	LM308AH	0 AMLM308AH
S = strobe	DIL-8/1P	F	E-	E+	٧-	N	R	٧+	F*	١.	.	١.		.	.				LM308AN	O AMLM308AN
T,T* = offset balance	DIL-14/1P	N	F	N	E-	E+	N	٧-	N	N	R	٧+	F*	N	N			UA308D	LM308D	0 AMLM308D
V+ = +ve dc supply	T05-8/1M	F	E-	E+	V-M	N	R	٧+	F*	١.	١.	١.	١.	.	.			SFC2308	LM308H	O AMLM308H
V- = $-$ ve dc supply	DIL-8/1P	F	E-	Ē+	٧-	N	R	٧+	F*	١.			١.		ا ا			SFC2308DC	LM308N	O AMLM308N
W = guard ring			1			] .	ļ		]	Ĭ .	ļ .		] `	] ]	<u> </u>	•				] ]
X = blank position, no lead	DIL-14/1P	N	N	т	N	E+	v-	N	N	L	R	٧+	T*	N	N			SFC2310EC	LM310D	0 AMLM3100
++ = +ve supplementary dc	FLP-10/30		T	N	E+	٧-	Ĺ	R	٧+	T*	N		Ι.			Ċ			LM310F	0 AMLM310F
supply	T05-8/1M	lΤ	N	E+	V-	Ĺ	R	v+	Т*	Ι΄.	l "			1				SFC2310EC	LM310H	0 AMLM310H
	DIL-8/1P	Ϊ́τ	N	E+	v-	Ĺ	R	V+	T*	Ι΄.	١ .		1		'			SFC2310DC	LM310N	O AMLM310N
—— ——ve supplementary dc	DIL-14/1F		G	E+	E-	N	٧-	T	¦ T*S	R	N.	۷+	N.	N	N			SFC2310BC	LM311D	O AMLM311D
supply	DIL 14/16	["	Ĭ	- '	-	"	Ι΄.	Ι΄	ا ' '	l''	''	١,,	١"	"	"	•		J. 0231120		I TIME MOTTE
ø. <b>¢*</b> = output frequency	T05-8/1M	G	E+	E-	y-	T	T*S	R	y+									SFC2311	LM311H	0 AMLM311H
compensation	DIL-14/1P		T	c	É-	E+	c*	\v	N	F	R.	v÷.	T+	N.	N.			5. 02011	LM312D	0 AMLM312D
	T05-8/1M	lΤ	E-	E+	۷-	F.	R	V+	ľ.	l'	"	l	Ľ	"	"				LM312H	0 AMLM312H
	DIL-14/1P	lù .	T	W	E-	E+	W*	٧- -۷	N N	F.	R.	٧+	T*	N.	N.			MLM316AD	LM312n	O AMLM316AD
	FLP-10/30		W		E+	W*	٧-	R	N V+	Ť	T*	۱′′	'	"	"	•		·ur-motoan	LM316AF	
	1 Lr 10/30	1"	"	-	10,	<sup>#</sup>	1	l"	1	Ι'	l '	١.	١.	1 .	.	•	i •		-m310AL	0 AMLM316AF

#### Appendix A

### Explanatory notes to tabulations

The general layout plan of the information in the tables of this compendium should be immediately evident from the data tabulation explanatory chart set out overleaf.

Supporting Appendices with additional information are:

```
App. B Glossary of Opamp Terms
App. C Tabulation Codes for Manufacturers
App. D IC Manufacturers' House Numbers
App. E Tabulation Codes for Applications
App. F Case Outline and Leadout Diagrams
App. G Codes for Leadout Connections
```

#### Unit symbols used in the tables are:

```
= amperes
С
      = ^{\circ} centigrade
      = decibels
      = gigaohms (megohms \times 10<sup>3</sup>)
GHZ = gigahertz (megahertz \times 10^3)
      = kilohms
KHZ = kilohertz
      = megohms
MA = milliamperes, mA
MAX = maximum
MHZ = megahertz
MIN = minimum
MV = millivolts
MWC = milliwatts, case at 25C
MWF = milliwatts, free air at 25C
MWH = milliwatts, heat sink, 25C
     = nanoamps (microamps \times 10<sup>-3</sup>)
NV
      = nanovolts (microvolts \times 10<sup>-3</sup>)
РΑ
      = picoamps (microamps \times 10<sup>-12</sup>)
R
      = ohms
      = teraohms (megohms \times 106)
WC
      = watts, case at 25C
      = watts, free air at 25C
WF
WH
      = watts, heatsink, 25C
      = microamps
μΑ
μS
      = microseconds
μV
      = microvolts
\mu W
      = microwatts
\mu WF = microwatts, free air at 25C
```

Where a unit symbol appears in the middle of a value, it indicates the position of the decimal point, e.g. 3K3 = 3.3K.

TYPE NO NUMERO-ALPHARETIC LISTING  MFR. MALPHABETIC LISTING  MFR. ALPHABETIC LISTING  MFR. MAY PER PAPELCATION CODED AS APP. C  COMPENSATION WITH INT. INTERNAL LIST. INT. INT. INT. INT. INT. INT. INT. IN	TYPE NUMBER	M F R	A P P	С <b>М</b> Р	GBP	SLEW RATE MIN	V <sub>S</sub> '	V <sub>S</sub> .	T <sub>op</sub> MAX	A <sub>VOL</sub> MIN	V <sub>IO</sub> MAX	I <sub>B</sub> MAX	I <sub>IO</sub> MAX	P <sub>TOT</sub> MAX	I <sub>OUT</sub> MIN	V <sub>OUT</sub> MIN	V <sub>ICM</sub> MAX	V <sub>IDF</sub>	dV <sub>IO</sub> ′dT MAX	P <sub>O</sub> MAX	I <sub>Q</sub> MAX	CM RR MIN	PS RR MIN	R <sub>IN</sub> MIN
NUMERO- ALPHABETIC LISTING  MFR = MANUFACTURER CODED AS APP. C  APP = APPLICATION COMEON ASTAIN NITH INTENIAL EXT = EXTERNAL  GBP MIN = UNITY GAIN BANDWIDTH PRODUCT, MIN: IN KHZ, MHZ, or GHZ  SLEW RATE MIN, IN VOLTS PER MICROSECOND, V/µS  V <sub>2</sub> MAX = MAX PERMISSIBLE +VE DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>3</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>4</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>5</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>6</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>7</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>8</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>9</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>2</sub> MAX = MAX PERMISSIBLE -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE OPERATIONAL -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE OPERATIONAL -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE OPERATIONAL -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>1</sub> MAX = MAX PERMISSIBLE OPERATIONAL -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>2</sub> MAX = MAX PERMISSIBLE OPERATIONAL -DC SUPPLY VOLTAGE IN VOLTS, V  V <sub>2</sub> MAX = MAX VIPPLIT OPERATIONS IN VOLTS, V  V <sub>3</sub> MAX = MAX VIPPLIT OPERATIONS IN VOLTS, V		NAU	FET	INT	. 3MHZ	1V/uS	+22٧	-227	85C	97d8	6м۷	25pA	5pA	500MWF	10 <b>M</b> A	100	15V	30V	15uV/C	85 <b>MW</b>	ЗМА	70 <b>d</b> B	70 <b>d</b> B	0.17
AT 25°C IN MV or μV.    AT 25	NUMERO-ALPHABETIC LISTING  MFR = MANUFACTU CODED AS AI  CODED AS AI  CMP = FREQU COMPENSATI INT = INTERN.  EXT = EXTERN  GBP MIN = UN  BANDWIDTH IN KHZ. MHZ.  SLEW RATE. IN TERN.  VS. MAX = MAY.  VE DC SUPI  VS. MAX = MAY.  AVOL MIN = MIN  AVOL MIN = MIN  AVOL MIN = GAI  VID MAX = MA  AT 25°C IN M  BMAX = MAX	PP. C  ATIO ATIO ATIO ATIO ATIO ATIO AL	OLT/ CRMINATULE  RMINATULE  RMINA	T, MI  T,	BLE IN VOL LE IN VOL ILE OPE I °C.	TS, V RATION										C=	W, m = CAS	I <sub>OUT</sub> N CURF X = M/ W, μV E 25°	dV, OF	O SIGMURREN POMAX NO SICPOWER OF AT METERS OF AT MAX MAX MAX MAX MON MON MON ARANTA AR	CMMAX.  AX = MAX  AX = MAX	F F F F F F F F F F F F F F F F F F F	MINI I WESTER AND IN THE STATE OF THE STATE	N- ANCE TION  I. RE- N DB  I MW  TURE IN V.  LE TAGE TPUT  TTUT

#### Appendix A

LEFT HAND PAGE For detailed explanations of column heading notations, see App. A. Also for ready references the more important abbreviations used in the column headings are listed below: APP = application (codes at APP.E.)  $\mathsf{CMRR} = \mathsf{common} \; \mathsf{mode}$ rejection ratio CMP = compensation (frequency)  $dV_{10}/dT$  = input offset voltage temperature drift  $\mathsf{GBP} \ = \mathsf{gain} \ \mathsf{bandwidth}$ product = input bias current = input bias offset 110 current = quiescent supply l<sub>o</sub> current MFR = manufacturer (codes at App.C.) = quiescent power consumer PSRR = power supply rejection ratio = common mode input voltage rating  $\mathbf{V}_{\text{IDF}}$ = differential input voltage rating  $V_{10}$ = input offset voltage = dc supply voltage RIGHT HAND PAGE Lead out coding summary (details at APP.G.) for different cases (APP.F.) Α = gain adjust В = bias adjust С = case E-= inverting input Ē+ = non-inverting input F,F\* = input frequency compensation G = ground = high level input = output, open collector = output, open emitter М = metal case N = not connected 0 = special terminal R.R\* = outputs = strobe S T,T\*

= offset balance

= +ve dc supply = -ve dc supply

= guard ring = blank position, no lead + + = +ve supplementary dc supply -- = -ve supplementary dc supply ø.♠\* = output frequency compensation

٧+

W

CASE (APP F)	LD 1	LD 2	LD 3	LD 4	LD 5	LD 6	LD 7	LD 8	LD 9	LD 10	LD 11	LD 12	LD 13	LD 14	LD 15	LD 16	EUROPE SUBSTI- TUTE	USA SUBSTI- TUTE		TYPE NUMBER
T05-8/1M	T	E-	E+	٧-	۲۰	R	۷+	N										LH0022H	o	LH00220
												į								
		\	\	\										/	/	/				
DIFFERENT CODED AC	ASE = PACKAGE OF IFFERENT TYPES DOED ACCORDING TO APP. F FIRST NUMBER INDICATES																		REP	E No. EATED ON MARGIN
NUMBER C EG DIL-14:	)F LEA = 14-1	AD P LEAD	OSIT	ION	s													ISS=IS OF DAT		NUMBEI NTRY
	D1, LD2, ETC=LEAD NUMBERS WITH CONNECTIONS																	STITUTE = TIVE AVAII		
ACCORDIN	CCORDING TO PAGE FOOTNOTE OR APP. G.												EURO SUBSTITUTE = PROELECTRON STANDAR OR OTHER TYPE AVAILABLE IN EUROPE							

### Appendix C

## Tabulation Codes for Manufacturers

A D.L.	Advanced Micro Devices Inc.,		DA145HT, UK
ADU	901 Thompson Pl., Sunnyvale, CA 94086, USA	ITU	ITT Semiconductors
ANG	Analog Devices Ltd,		74 Commerce Way, Woburn, MA, 01801, USA
ANG	Central Ave., East Molesey, KT8 9BR, Surrey,	MNG	Mitsubishi Shoji Kaisha Ltd,
	UK		Bow Bells House, Bread St., London, EC4, UK
ANU	Analog Devices Inc.,	MNJ	Mitsubishi Electric Corp.,
7110	P.O. Box 280, Norwood, Mass., 02062		2–12 Marunouchi, Chiyoda-ku, Tokyo, Japan
BLG	Bell & Howell Ltd,	MTG	Motorola Ltd (Semiconductor Products Div.),
0_0	Lennox Road, Basingstoke, Hants, UK		York House, Empire Way, Wembley, Middlesex,
BLU	Bell & Howell (Control Products Divison),		HA9 OPR, UK
	706 Bostwick Ave, Bridgeport, Conn. 06605,	MTU	Motorola Semiconductor Products Inc.,
	USA		5005 E. McDowell Road, Phoenix, AZ, 85008,
BUG	Burr-Brown International Ltd,		USA
	17 Exchange Rd, Watford, WQD1 7EB, Herts.,	MUG	Mullard Ltd,
	UK		Mullard House, Torrington Place, London,
BUU	Burr-Brown Research Corp.,		WC1E7HD, UK
	P.O. Box 11400, Tucson, AZ. 85734, USA	NAG	National Semiconductor (UK) Ltd,
CMG	Computing Techniques Ltd,		Harpur Centre, Bedford, MK40 3LF, UK
	Brookers Rd, Billingshurst, Sussex, RH14 9RZ,	NAU	National Semiconductor Corp., 2900 Semiconductor Drive, Santa Clara, CA,
	UK		
DAG	Datel UK Ltd,		95051, USA
	Stephenson Close, Portway Ind. Estate,	NIJ	Nippon Electric Co. Ltd, 1753 Shimonumabe, Nakahara-ku, Kawasaki,
	Andover, Hants, UK		
DAU	Datel Systems Inc.,	OAU	Japan Opamp Labs Inc.,
E A C	1020 Turnpike St., Canton, MA 02021, USA	UAU	1033 N. Sycamore Ave., Los Angeles, CA
FAG	Fairchild Camera & Instrument (UK) Ltd, 230 High St., Potters Bar, Herts., UK		90038, USA
FAU	Fairchild Semiconductor	OBS	Obsolete – no longer commercially available.
FAU	464 Ellis St., Mountain View, CA 94042, USA	OTU	Optical Electronics Inc.,
FEG	Ferranti Ltd, (Electronic Department),	0.0	P.O. Box 11140, Tucson, AZ, 85734, USA
1 L G	Gem Mill, Chadderton, Oldham, Lancs.,	PLG	Plessey Semiconductors.
	OL9 8NP, UK		Cheney Manor, Swindon, Wilts., SN2 2QW, UK
FUJ	Fujitsu Ltd,	PRG	Precision Monolithics (Bourns Trimpot Ltd)
	1015 Kamikodanaka, Kawasaki, Japan		17/27 High St., Hounslow, Middlesex, UK
HAG	Harris Semiconductor (Memec) Ltd,	PRU	Precision Monolithics (Bourns) Inc.,
	The Firs, Whitchurch, Nr. Aylesbury, Bucks.,		1500 Space Park Drive, Santa Clara, CA,
	HP22 4JU, UK		95050, USA
HAU	Harris Semiconductor	RAG	Raytheon Semiconductor
	P.O. Box 883, Melbourne, FL, 32901, USA		The Pinnacles, Harlow, Essex, CM19 5BB, UK
HIJ	Hitachi Ltd (Semiconductor and IC Div.),	RAU	Raytheon Semiconductor,
	1450 Josuihonimachi, Kodaira City, Tokyo,		350 Ellis Street, Mountain View, CA, 94042,
	Japan	000	USA
ING	Intersil Inc.,	RCG	RCA (Great Britain) Ltd, Lincoln Way, Windmill Road, Sunbury-on-
	8 Tessa Rd, Richfield Trading Estate, Reading,		Thames, Middlesex, UK
LNU	Berks., UK	RCU	RCA Solid State Division
INU	Intersil Inc., 10900 N. Tantau Ave, Cupertino, CA, 95014,	nco	Route 202, Somerville, NJ, 08876, USA
	USA	SAJ	Sanken Electric Co. Ltd,
ITG	ITT Semiconductors	0,10	1-22-8 Nishi-Ikebukuro, Toshima-Ku, Tokyo,
,10	Maidstone Rd Foots Cray Sideup Kent		Japan

SGG	SGS-ATES (UK) Ltd, Planar House, Walton Street, Aylesbury, Bucks., UK	SPU	Sprague Electric Company (Semiconductor Div.), 115 Northeast Cutoff, Worcester, MA, 01606,
SGI	SGS-ATES Componenti Spa, Via Olivetti, 2 Agrate Brianza, 20041, Milan,	TDG	USA Teledyne Semiconductor,
SHG	Italy Shindengen Hyokuto Boeki Haisha Ltd, St. Alphage House, Fore St., London, EC2Y 5DA,	TDU	Heathrow House, Bath Road, Cranford, Hounslow, Middlesex, TW5 9QP, UK Teledyne (Amelco) Semiconductor,
SHJ	UK Shindengen Electric Mfg Co., Ltd,	100	1300 Terra Bella Ave, Mountain View, CA, 94032, USA
	New Ohtemachi Bldng, 2–1, 2-chome, Ohtemachi, Chiyoda-ku, Tokyo, Japan	TEB	Teledyne-Philbrick, Heathrow House, Bath Road, Cranford, Houns-
SIG	Siemens Ltd, Great West Road, Brentford, Middlesex, TW8 9DG, UK	TEU	low, Middlesex, TW5 9QP, UK <b>Teledyne-Philbrick,</b> Allied Drive at Route 128, Dedham, MA, 02026,
SIW	Siemens Aktiengesellschaft, Richard-Strauss-Strasse 76, D-8000 Munchen	TGG	USA Texas Instruments Ltd,
SJG	2, Postfach 202109, W. Germany Signetics International Corporation Yeoman House, 63 Croydon Rd, London, SE20,	TGU	Manton Lane, Bedford, UK <b>Texas Instruments Inc.</b> (Components Group), P.O. Box 5012, Dallas, Texas, 75222, USA
SJU	UK Signetics Corp.,	THF	Thomson-CSF (Sescosem), 50 Rue Jean Pierre Timbaud, BP 120, 92403,
01/11	811 East Arques Ave, Sunnydale, CA. 94086, USA	THG	Courbevoie, France Thomson-CSF (UK) Ltd, Ringway House, Bell Rd, Daneshill, Basing-
SKU	Silicon General Inc., 7382 Bolsa Avenue, Westminster, CA, 92683, USA	TKJ	stoke, Hants., RG24 0QG, UK.  Tokyo Sanyo Electric Co. Ltd (Semiconductor
SLG	Siliconix Ltd, 30A High St., Thatcham, Newbury, Berks.,		Div.), Oizumachi, Oragun, Gumma, Japan
SLU	RG13 4JG, UK  Siliconix Incorporated, 2201 Laurelwood Road, Santa Clara, CA.	TOG	Toshiba (UK) Ltd, Toshiba House, Great South West Rd, Feltham, Middlesex, UK
soJ	95054, USA Sony Semiconductor Corp.,	TOJ	Toshiba (Tokyo Shibaura) Electric Co., 2–1, 5-chome, Ginza Chuo-ku, Tokyo, Japan
	14—1, Asa hi-sho 4, Atsuigi-shi, Kanagawa-ken, 243, Japan	TRU	Transitron Electronic Corp., 168 Albion St., Wakefield, MA, 01881, USA
SPG	Sprague Electric (UK) Ltd, 159 High St., Yiewsley, W. Drayton, Middlesex, UB7 7RY, UK	ZEU	Zeltex Inc., 940 Detroit Ave, Concord, CA, 94518, USA

#### Appendix D IC Manufacturers' House Numbers

OP

(General Note: Manufacturers often adopt their own 'in-house' serial numbering for their ICs. Listed below are the initial letters of numerical series used by different manufacturers.)

**Analog Devices** RA ADO Analog Devices RC Advanced Micro Devices; Datel AM AMD Advanced Micro Devices RM **AMLM** Advanced Micro Devices **RSN AMSSS** Advanced Micro Devices RV **AMU** Advanced Micro Devices Bell & Howell SA CA **RCA** SE CIA Teledyne-Philbrick **SFC** CMP Precision Monolithics SG CN Ferranti SH Teledyne-Philbrick DA Teledyne-Philbrick EΡ SL **ESL** Teledyne-Philbrick SN **FSL** Teledyne-Philbrick **FSS** Ferranti sa SSS Harris HA HEPC Motorola SU ICH Intersil ICL Intersil TA Fairchild TAA JM Thomson-CSF JSF TBA Analog Devices; SGS-ATES TBB LA Teledyne-Philbrick TBC LF National Semiconductor TBE LH National Semiconductor TCA National Semiconductor TDA LM TDB Mitsubishi TDC Motorola Semiconductors MC мсс Motorola Semiconductors TDE MCCF Motorola Semiconductors TOA Motorola Semiconductors MCE **MCH** Motorola Semiconductors **TSC** MIC ITT Semiconductors MLF Motorola: Teledyne-Philbrick ULN Motorola Semiconductors MLM ULS MLMC Motorola Semiconductors USL MONO-OP **Precision Monolithics** Signetics: Mullard ZEL General Instruments (obs.) NC ZLD ΖN Signetics; Mullard National Semiconductor μΑ

Precision Monolithics Teledyne-Philbrick Teledyne-Philbrick ΡF PG General Instruments (obs.) Teledyne-Philbrick Radiation (now Harris) Raytheon Raytheon Raytheon Raytheon Raytheon **Signetics** Teledyne-Philbrick Signetics; Mullard Thomson-CSF Silicon General Fairchild RCA Plessey: Teledyne-Philbrick Texas Instruments Teledyne-Philbrick Teledyne-Philbrick **Precision Monolithics** Signetics; Mullard Teledyne-Philbrick Transitron AEG-Telefunken Proelectron Standard AEG-Telefunken Transitron Transitron Fairchild Sprague Sprague Teledyne-Philbrick Zeltex Zeltex Ferranti Ferranti

Fairchild

# Appendix E Tabulation Codes for Applications

BDO	Balanced differential-output amplifier	PAA	Parametric amplifier
CDA	Current-difference amplifier	PIA	Precision instrumentation amplifier
CHP	Chopper-stabilized amplifier	PRA	Programmable opamp
CPR	DC comparator	QCD	Quad current-difference amplifier
DBD	Dual balanced differential-output amplifier	QCP	Quad comparator
DCP	Dual Comparator	QFE	Quad fet-input opamp
DFE	Dual fet-input opamp	QGK	Quadgeneral-purpose, internally-compensated,
DGK	Dual general purpose opamp	2011	opamp
DGU	Dual general-purpose uncompensated opamp	QGU	Quad general-purpose, uncompensated, opamp
DHS	Dual high-slew-rate opamp	gra	Quad low-quiescent-power opamp
DLN	Dual low-noise opamp	QPI	Quad precision instrumentation amplifier
DPI	Dual precision instrumentation amplifier	QPR	Quad programmable opamp
DPR	Dual programmable opamp	QSB	Quad super-beta opamp
DSB	Dual super-beta opamp	SBA	Super-beta opamp
FET	Fet-input opamp	TCP	Triple comparator
GPK	General-purpose, internally-compensated,	TFE	Triple fet-input opamp
J	opamp	TGK	Triple general-purpose, internally compensated,
GPU	General-purpose, uncompensated, opamp		opamp
HCO	High current output opamp	TGU	Triple general-purpose, uncompensated, opamp
HIR	High input resistance opamp	TLN	Triple low-noise opamp
HPO	High power output opamp	TLP	Triple low-quiescent-power opamp
HSR	High slew rate opamp	TOT	Triple operational transconductance amplifier
HVO	High voltage output opamp	TPI	Triple precision instrumentation amplifier
LBC	Low input bias current opamp	TPR	Triple programmable opamp
LCD	Low input offset current drift opamp	TSB	Triple super-beta opamp
LNA	Low noise opamp	VFA	Voltage-follower amplifier
LOC	Low input offset current opamp	WBA	Wide-band opamp
ĽOV	Low input offset voltage opamp	XHG	Extra-high-gain opamp
LQP	Low guiescent power opamp	XLP	Extra-low quiescent power opamp
LVD	Low input offset voltage drift opamp	XSR	Extra-high slew rate opamp
MWB	Medium-wideband opamp	XWB	Extra-wide-band opamp
OTA	Operational transconductance amplifier		

## Appendix G Codes for Leadout Connections

```
Connection Codes in Serial Order
                                                             II: Lead Assignments in Alphabetical Order
   = Gain adjust, 1
                                                             Balance, offset, 1 = T
   = Gain adjust, 2
                                                             Balance, offset, 2 = T^*
   = Bias adjust or set
                                                             Bias adjust = B
                                                             Blank position, without lead = X
   = Case, package, screen
   = Input, non-inverting, low-level
                                                             Case = C
   = Input, inverting, low-level
                                                             Compensation, input, 1 = F
   = Input frequency compensation, 1
                                                             Compensation, input, 2 = F^*
   = Input frequency compensation, 2
                                                             Compensation, output, 1 = \varphi
   = Ground, common, earth, zero volts
                                                             Compensation, output, 2 = \phi^*
   = Input, non-inverting, high-level
                                                             DC supply, + ve = V +
   = Input, inverting, high-level
                                                             DC supply, -ve = V -
   = Output, open collector
                                                             Frequency compensation, input, 1 = F
   = Output, open emitter
                                                             Frequency compensation, input, 2 = F*
   = Metal casing
                                                             Frequency compensation, output, 1 = \varphi
   = Not connected, i.e. isolated lead
                                                             Frequency compensation, output, 2 = \phi^*
                                                            Gain adjust, 1 = A
Gain adjust, 2 = A^*
   = Special terminal (consult manufacturer's data)
   = Output, 1
   = Output, 2
                                                             Ground = G
   = Strobe
                                                             Guard ring=W
                                                             Input, inverting, high-level=J
   = Offset balance, trim or null, 1
                                                             Input, non-inverting, high-level = J+
   = Offset balance, trim or null, 2
   = + ve dc supply
                                                             Input, inverting, low-level = E-
   = -ve dc supply
                                                             Input, non-inverting, low-level = E+
   = Guard ring
= Blank position, lead omitted
                                                             Input offset voltage, adjust, 1 = T
                                                             Input offset voltage, adjust, 2=T*
   = + ve supplementary dc supply
                                                             Lead omitted, blank position = X
   = -ve supplementary dc supply
                                                             Lead in position but not connected = N
   = Output frequency compensation, 1
                                                             Metal case = M
   = Output frequency compensation, 2
                                                             Not connected, but lead in position = N
                                                            Null, offset, 1=T
Null, offset, 2=T*
                                                            Offset voltage adjust, 1=T
Offset voltage adjust, 2=T*
Output, 1=R
Output, 2=R*
                                                             Output, open-collector = K
                                                             Output, open-emitter = L
                                                             Package = C
                                                             Special purpose terminal (data sheet to be consulted) = Q
                                                             Strobe = S
                                                             Supply, dc, +ve=V+
                                                             Supply, dc, -ve = V -
                                                            Supply, dc, supplementary, +ve=++
Supply, dc, supplementary, -ve=--
Trim (offset voltage), 1 = T
                                                             Trim (offset voltage), 2 = T^*
```



