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Specifications and Applications Information

THREE-TERMINAL ADJUSTABLE **OUTPUT POSITIVE VOLTAGE REGULATORS**

The LM117L/217L/317L are adjustable 3-terminal positive voltage regulators capable of supplying in excess of 100 mA over an output voltage range of 1.2 V to 37 V. These voltage regulators are exceptionally easy to use and require only two external resistors to set the output voltage. Further, they employ internal current limiting, thermal shutdown and safe area compensation, making them essentially blow-out proof.

The LM117L series serves a wide variety of applications including local, on card regulation. This device can also be used to make a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM117L series can be used as a precision current regulator.

- Output Current in Excess of 100 mA
- Output Adjustable Between 1.2 V and 37 V
- Internal Thermal Overload Protection
- Internal Short-Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Floating Operation for High Voltage Applications
- Standard 3-Lead Transistor Packages
- Eliminates Stocking Many Fixed Voltages

LM117L

T-58-11-23

LOW-CURRENT THREE-TERMINAL **ADJUSTABLE POSITIVE VOLTAGE REGULATORS**

> SILICON MONOLITHIC INTEGRATED CIRCUIT

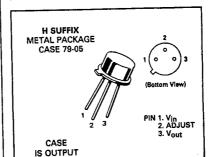
Z SUFFIX PLASTIC PACKAGE **CASE 29-04** (LM317 only)





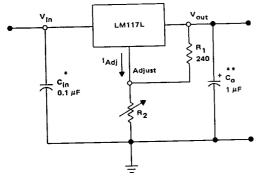
PLASTIC PACKAGE CASE 751-03





ORDERING INFORMATION							
Davice	Tested Operating Temperature Range	Package					
LM117LH	T _J = -55°C to +150°C	Metal Can					
LM217LH	T _J = -25°C to +150°C	Metal Can					
LM317LH		Metal Can					
LM317LZ	Tj = 0°C to +125°C	Plastic					

STANDARD APPLICATION



- = Cin is required if regulator is located an appreciable distance from power
- supply filter.
- Co is not needed for stability, however it does improve transient response.

 $V_{out} = 1.25 \text{ V } (1 + \frac{R_2}{R_1}) + I_{Adj} R_2$

Since I_{Adj} is controlled to less than 100 μA , the error associated with this term is negligible in most applications

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LM117L, LM217L, LM317L

MAXIMUM RATINGS

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Rating Input-Output Voltage Differential		Symbol	Value	Unit	
		V _I -V _O	40	Vdc	
Power Dissipation		PD	Internally Limited		
Operating Junction Temperature Range	LM117L LM217L LM317L	TJ	-55 to +150 -25 to +150 0 to +150	°C	
Storage Temperature Range		T _{stg}	-65 to +150	°C	

ELECTRICAL CHARACTERISTICS

(VI - VO = 5.0 V; IO = 40 mA; T.I = Tlow to Thigh [see Note 1]; Imax and P-

$(V_I - V_O = 5.0 \text{ V}; I_O = 40 \text{ mA}; T_J = T_{IOW} \text{ t}$	Figure	Symbol	LM117L/217L			LM317L			
Characteristic			Min	Тур	Max	Min	Тур	Max	Unit
Line Regulation (Note 3) $T_A = 25^{\circ}C$, 3.0 V \leq V _I $-$ V _O \leq 40 V	1	Regline		0.01	0.02	_	0.01	0.04	%∕V
Load Regulation (Note 3), T _A = 25°C 5.0 mA ≤ I _O ≤ I _{max} — LM117L/217L 10 mA ≤ I _O ≤ I _{max} — LM317L V _O ≤ 5.0 V	2	Regiond	_	5.0	15	_	5.0	25	mV
V _O ≥ 5.0 V			 - -	0.1	0.3		0.1	0.5	% Vo
Adjustment Pin Current	3	^l Adj	ļ <u>-</u> -	50	100	<u> </u>	50	100	μA
Adjustment Pin Current Change 2.5 V ≤ V _I − V _O ≤ 40 V, P _D ≤ P _{max} 5.0 mA ≤ I _O ≤ I _{max} − LM117L/217L 10 mA ≤ I _O ≤ I _{max} − LM317L	1,2	ΔlAdj	_	0.2	5.0	_	0.2	5.0	μΑ
Reference Voltage (Note 4) 3.0 V ≤ V _I − V _O ≤ 40 V, P _D ≤ P _{max} 5.0 mA ≤ I _O ≤ I _{max} — LM117L/217L 10 mA ≤ I _O ≤ I _{max} — LM317L	3	V _{ref}	1.20	1.25	1.30	1.20	1.25	1.30	V
Line Regulation (Note 3) 3.0 $V \le V_1 - V_0 \le 40 \text{ V}$	1	Regline	_	0.02	0.05	_	0.02	0.07	%/V
Load Regulation (Note 3) 5.0 mA ≤ _O ≤ _{max} — LM117L/217L 10 mA ≤ _O ≤ _{max} — LM317L V _O ≤ 5.0 V	2	Regload	_	20	50	_	20	70	mV
V _O > 5.0 V	ļ			0.3	1.0		0.3	1.5	% Vo
Temperature Stability ($T_{low} \le T_J \le T_{high}$)	3	TS		0.7		_	0.7	_	% Vo
Minimum Load Current to Maintain Regulation (V _I – V _O = 40 V)	3	Lmin		3.5	5.0	1	3.5	10	mA
Maximum Output Current $V_1-V_0 \le 20 \text{ V}, P_D \le P_{max}, H \text{ Package}$ $V_1-V_0 \le 6.25 \text{ V}, P_D \le P_{max}, Z \text{ Package}$ $V_1-V_0 = 40 \text{ V}, P_D \le P_{max}, T_A = 25^{\circ}\text{C}$	3	I _{max}	100 100	200 200		100 100	200 200	<u>-</u>	mA
H Package Z Package			_	50 20	_	_	50 20	_	1
RMS Noise, % of V_0 $T_A = 25^{\circ}C$, 10 Hz $\leq f \leq$ 10 kHz	-	N	-	0.003	-		0.003	_	% Vo
Ripple Rejection (Note 5) V _O = 1.25 V, f = 120 Hz C _{ADJ} = 10 μF V _O = 10.0 V	4	RR	66 —	80 80	<u>-</u>	60 —	80 80	-	dB
Long Term Stability, T _J = T _{high} (Note 6) T _A = 25°C for Endpoint Measurements	3	S	_	0.3	1.0		0.3	1.0	%/1.0 k Hrs.
Thermal Resistance Junction to Case H Package Z Package	_	R _{NC}	_	40 —	_	_	40 83	_	°C/W
Thermal Resistance Junction to Air H Package Z Package	_	R _{ØJA}	· <u>-</u>	185		_	185 160	_	°C/W

NOTES: (1) T_{low} = -55°C for LM117L -25°C for LM217L 0°C for LM317L

Thigh = +150°C for LM117L = +150°C for LM217L = +125°C for LM317L

(2) I_{max} = 100 mA P_{max} = 2 W for H Package = 625 mW for Z Package

(3) Load and line regulation are specified at constant junction temperature. (a) Lead another egulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.
 (4) Selected devices with tightened tolerance reference voltage available.
 (5) CADJ, when used, is connected between the adjustment pin and ground.
 (6) Since Long Term Stability cannot be measured on each device before shipment, this specification is an engineering estimate of average.

shipment, this specification is an engineering estimate of average stability from lot to lot.

LM117L, LM217L, LM317L

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SCHEMATIC DIAGRAM

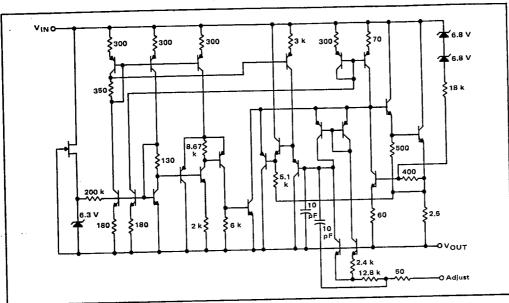
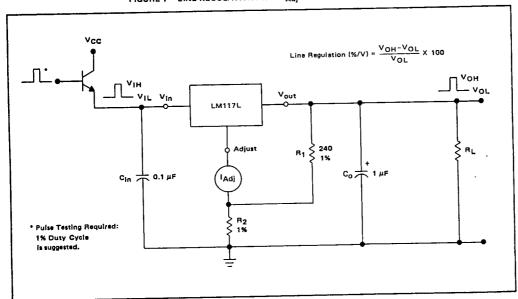


FIGURE 1 — LINE REGULATION AND $\Delta I_{\mbox{Adj}}/\mbox{LINE TEST CIRCUIT}$



LM117L, LM217L, LM317L

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FIGURE 2 – LOAD REGULATION AND $\Delta I_{Adj}/LOAD$ TEST CIRCUIT

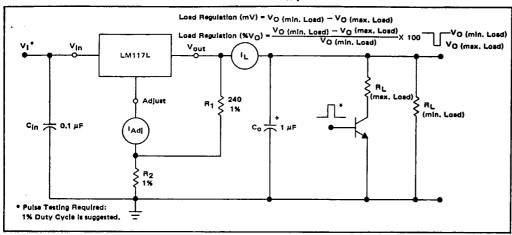


FIGURE 3 - STANDARD TEST CIRCUIT

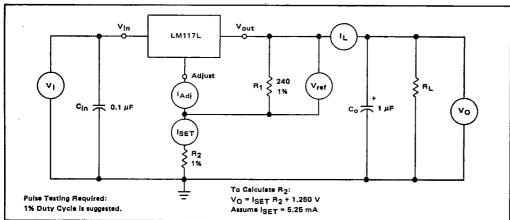
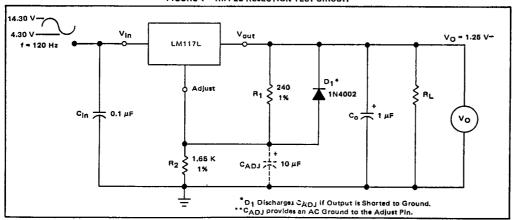


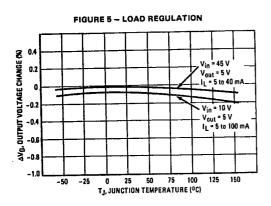
FIGURE 4 - RIPPLE REJECTION TEST CIRCUIT

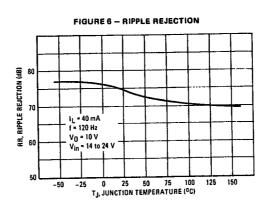


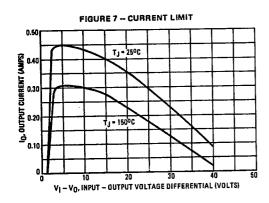
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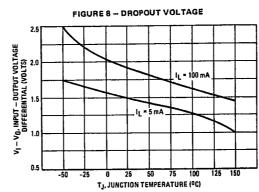
LM117L, LM217L, LM317L

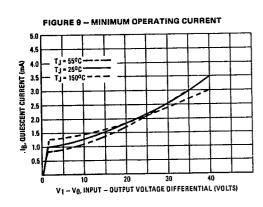
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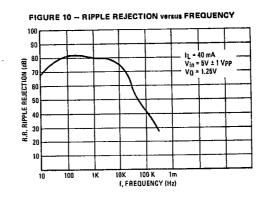








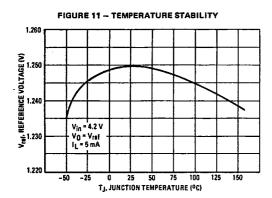


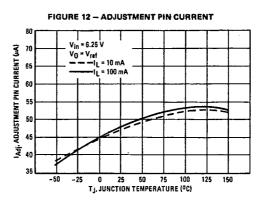


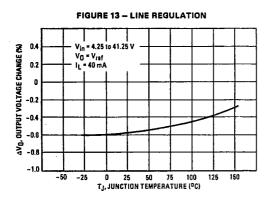
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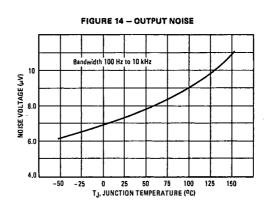
LM117L, LM217L, LM317L

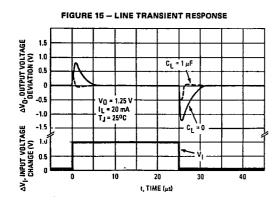
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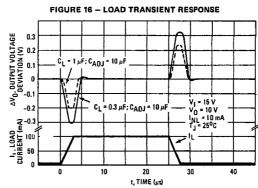












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APPLICATIONS INFORMATION

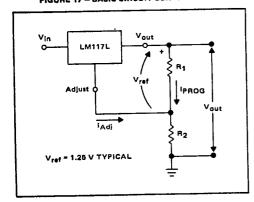
BASIC CIRCUIT OPERATION

The LM117L is a 3-terminal floating regulator. In operation, the LM117L develops and maintains a nominal 1.25 volt reference (Vref) between its output and adjustment terminals. This reference voltage is converted to a programming current (IPROG) by R1 (see Figure 13), and this constant current flows through R2 to ground. The regulated output voltage is given by:

Since the current from the adjustment terminal (IAdj) represents an error term in the equation, the LM117L was designed to control IAdj to less than 100 μ A and keep it constant. To do this, all quiescent operating current is returned to the output terminal. This imposes the requirement for a minimum load current. If the load current is less than this minimum, the output voltage will rise.

Since the LM117L is a floating regulator, it is only the voltage differential across the circuit which is important to performance, and operation at high voltages with respect to ground is possible.

FIGURE 17 - BASIC CIRCUIT CONFIGURATION



LOAD REGULATION

The LM117L is capable of providing extremely good load regulation, but a few precautions are needed to obtain maximum performance. For best performance, the programming resistor (R1) should be connected as close to the regulator as possible to minimize line drops which effectively appear in series with the reference, thereby degrading regulation. The ground end of R2 can be returned near the load ground to provide remote ground sensing and improve load regulation.

EXTERNAL CAPACITORS

A 0.1 μ F disc or 1 μ F tantalum input bypass capacitor (C_{in}) is recommended to reduce the sensitivity to input line impedance.

The adjustment terminal may be bypassed to ground to improve ripple rejection. This capacitor (CADJ) prevents ripple from being amplified as the output voltage is increased. A 10 µF capacitor should improve ripple rejection about 15dB at 120 Hz in a 10 volt application.

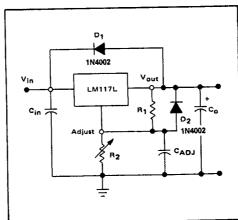
Although the LM117L is stable with no output capacitance, like any feedback circuit, certain values of external capacitance can cause excessive ringing. An output capacitance (C_0) in the form of a 1 μ F tantalum or 25 μ F aluminum electrolytic capacitor on the output swamps this effect and insures stability.

PROTECTION DIODES

When external capacitors are used with any I.C. regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator.

Figure 14 shows the LM117L with the recommended protection diodes for output voltages in excess of 25 V or high capacitance values ($C_0 > 10~\mu\text{F}$, $C_{ADJ} > 5~\mu\text{F}$). Diode D₁ prevents C₀ from discharging thru the I.C. during an input short circuit. Diode D₂ protects against capacitor C_{ADJ} discharging through the I.C. during an output short circuit. The combination of diodes D1 and D2 prevents C_{ADJ} from discharging through the I.C. during an input short circuit.

FIGURE 18 - VOLTAGE REGULATOR WITH PROTECTION DIODES



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FIGURE 19 - ADJUSTABLE CURRENT LIMITER

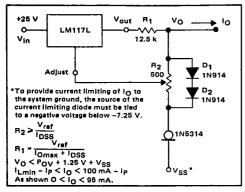


FIGURE 21 — SLOW TURN-ON REGULATOR

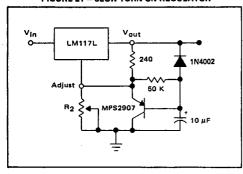


FIGURE 20 - 5 V ELECTRONIC SHUTDOWN REGULATOR

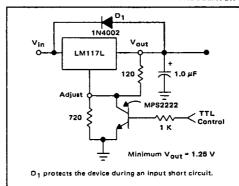
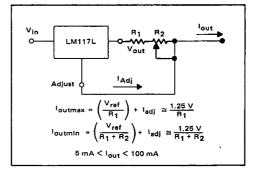


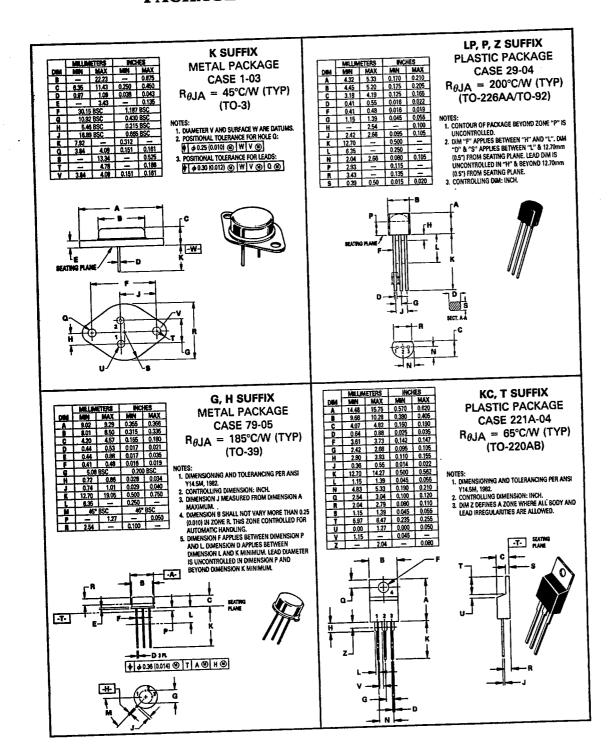
FIGURE 22 - CURRENT REGULATOR

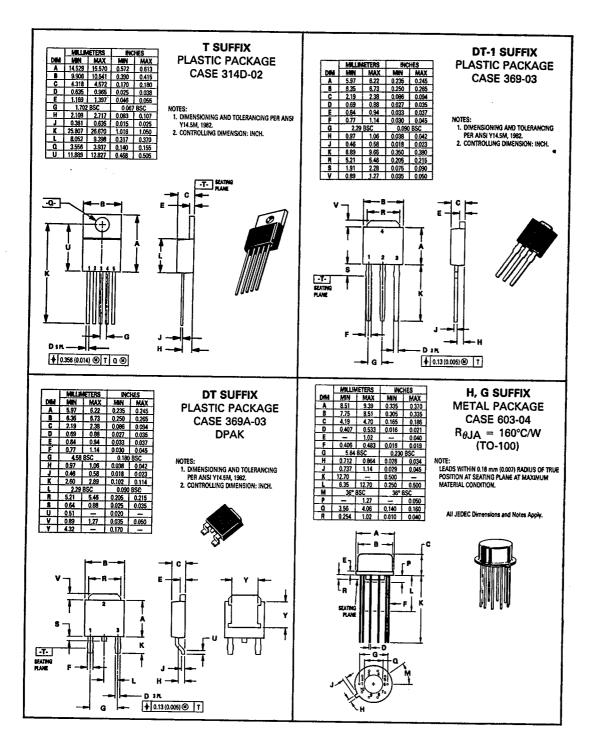


SECTION 19

T-90-20

PACKAGE OUTLINE DIMENSIONS





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