

SN54HC367, SN74HC367 HEX BUFFERS AND LINE DRIVERS WITH 3-STATE OUTPUTS

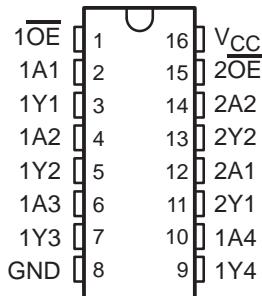
SCLS309D – JANUARY 1996 – REVISED SEPTEMBER 2003

- Wide Operating Voltage Range of 2 V to 6 V
- High-Current 3-State Outputs Drive Bus Lines, Buffer Memory Address Registers, or Drive Up To 15 LSTTL Loads
- True Outputs
- Low Power Consumption, 80- μ A Max I_{CC}
- Typical $t_{pd} = 10$ ns
- ± 6 -mA Output Drive at 5 V
- Low Input Current of 1 μ A Max

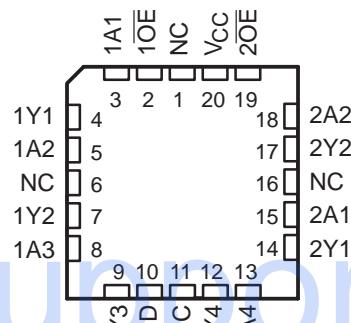
description/ordering information

These hex buffers and line drivers are designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. The 'HC367 devices are organized as dual 4-line and 2-line buffers/drivers with active-low output-enable (\overline{OE} and $\overline{2OE}$) inputs. When \overline{OE} is low, the device passes noninverted data from the A inputs to the Y outputs. When \overline{OE} is high, the outputs are in the high-impedance state.

SN54HC367 . . . J OR W PACKAGE
SN74HC367 . . . D, N, NS, OR PW PACKAGE
(TOP VIEW)



SN54HC367 . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

ORDERING INFORMATION

TA	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
–40°C to 85°C	PDIP – N	Tube of 25	SN74HC367N	SN74HC367N
	SOIC – D	Tube of 40	SN74HC367D	HC367
		Reel of 2500	SN74HC367DR	
		Reel of 250	SN74HC367DT	
	SOP – NS	Reel of 2000	SN74HC367NSR	HC367
	TSSOP – PW	Tube of 90	SN74HC367PW	HC367
		Reel of 2000	SN74HC367PWR	
		Reel of 250	SN74HC367PWT	
–55°C to 125°C	CDIP – J	Tube of 25	SNJ54HC367J	SNJ54HC367J
	CFP – W	Tube of 150	SNJ54HC367W	SNJ54HC367W
	LCCC – FK	Tube of 55	SNJ54HC367FK	SNJ54HC367FK

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

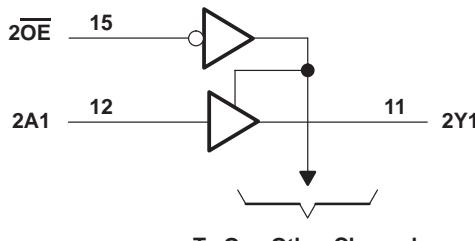
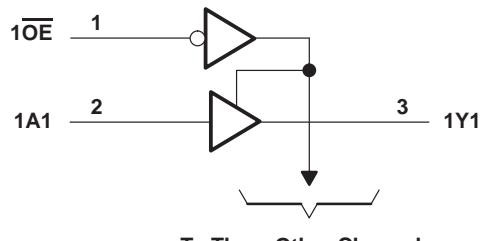
SN54HC367, SN74HC367 HEX BUFFERS AND LINE DRIVERS WITH 3-STATE OUTPUTS

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FUNCTION TABLE
(each buffer/driver)

INPUTS		OUTPUT
\overline{OE}	A	Y
H	X	Z
L	H	H
L	L	L

logic diagram (positive logic)



Pin numbers shown are for the D, J, N, NS, PW, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC}	-0.5 V to 7 V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$) (see Note 1)	± 20 mA
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) (see Note 1)	± 20 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	± 35 mA
Continuous current through V_{CC} or GND	± 70 mA
Package thermal impedance, θ_{JA} (see Note 2): D package	73°C/W
N package	67°C/W
NS package	64°C/W
PW package	108°C/W
Storage temperature range, T_{stg}	-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. The package thermal impedance is calculated in accordance with JEDEC 51-7.

**SN54HC367, SN74HC367
HEX BUFFERS AND LINE DRIVERS
WITH 3-STATE OUTPUTS**

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recommended operating conditions (see Note 3)

			SN54HC367			SN74HC367			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	Supply voltage		2	5	6	2	5	6	V
V _{IH}	High-level input voltage	V _{CC} = 2 V	1.5		1.5				V
		V _{CC} = 4.5 V	3.15		3.15				
		V _{CC} = 6 V	4.2		4.2				
V _{IL}	Low-level input voltage	V _{CC} = 2 V		0.5		0.5			V
		V _{CC} = 4.5 V		1.35		1.35			
		V _{CC} = 6 V		1.8		1.8			
V _I	Input voltage		0	V _{CC}		0	V _{CC}		V
V _O	Output voltage		0	V _{CC}		0	V _{CC}		V
Δt/Δv	Input transition rise/fall time	V _{CC} = 2 V		1000		1000			ns
		V _{CC} = 4.5 V		500		500			
		V _{CC} = 6 V		400		400			
T _A	Operating free-air temperature		-55	125	-40	85			°C

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V_{CC}	T_A = 25°C			SN54HC367		SN74HC367	UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	
V _{OH}	V _I = V _{IH} or V _{IL}	I _{OH} = -20 μA	2 V	1.9	1.998	1.9		1.9	V
			4.5 V	4.4	4.499	4.4		4.4	
			6 V	5.9	5.999	5.9		5.9	
		I _{OH} = -6 mA	4.5 V	3.98	4.3	3.7		3.84	
		I _{OH} = -7.8 mA	6 V	5.48	5.8	5.2		5.34	
V _{OL}	V _I = V _{IH} or V _{IL}	I _{OL} = 20 μA	2 V	0.002	0.1	0.1		0.1	V
			4.5 V	0.001	0.1	0.1		0.1	
			6 V	0.001	0.1	0.1		0.1	
		I _{OL} = 6 mA	4.5 V	0.17	0.26	0.4		0.33	
		I _{OL} = 7.8 mA	6 V	0.15	0.26	0.4		0.33	
I _I	V _I = V _{CC} or 0	6 V	±0.1	±100		±1000		±1000	nA
I _{OZ}	V _O = V _{CC} or 0	6 V	±0.01	±0.5		±10		±5	μA
I _{CC}	V _I = V _{CC} or 0, I _O = 0	6 V		8		160		80	μA
C _i		2 V to 6 V	3	10		10		10	pF

**SN54HC367, SN74HC367
HEX BUFFERS AND LINE DRIVERS
WITH 3-STATE OUTPUTS**

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switching characteristics over recommended operating free-air temperature range, $C_L = 50 \text{ pF}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CC}	$T_A = 25^\circ\text{C}$			SN54HC367	SN74HC367	UNIT
				MIN	TYP	MAX	MIN	MAX	
t_{pd}	A	Y	2 V	50	95	145	120	120	ns
			4.5 V	12	19	29	24	24	
			6 V	10	16	25	20	20	
t_{en}	\overline{OE}	Y	2 V	100	190	285	238	238	ns
			4.5 V	26	38	57	48	48	
			6 V	21	32	48	41	41	
t_{dis}	\overline{OE}	Y	2 V	50	175	265	240	240	ns
			4.5 V	21	35	53	48	48	
			6 V	19	30	45	41	41	
t_t		Any	2 V	28	60	90	75	75	ns
			4.5 V	8	12	18	15	15	
			6 V	6	10	15	13	13	

switching characteristics over recommended operating free-air temperature range, $C_L = 150 \text{ pF}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CC}	$T_A = 25^\circ\text{C}$			SN54HC367	SN74HC367	UNIT
				MIN	TYP	MAX	MIN	MAX	
t_{pd}	A	Y	2 V	70	120	180	150	150	ns
			4.5 V	17	24	36	30	30	
			6 V	14	20	31	25	25	
t_{en}	\overline{OE}	Y	2 V	140	230	345	285	285	ns
			4.5 V	30	46	69	57	57	
			6 V	28	39	59	48	48	
t_t		Any	2 V	45	210	315	265	265	ns
			4.5 V	17	42	63	53	53	
			6 V	13	36	53	45	45	

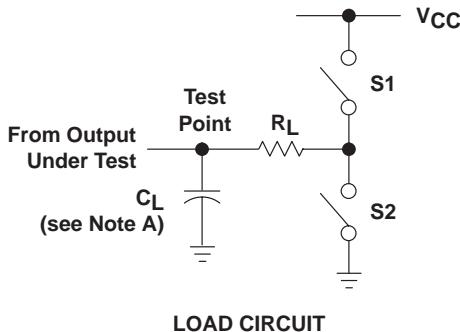
operating characteristics, $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	TYP	UNIT
C_{pd} Power dissipation capacitance per buffer/driver	No load	35	pF

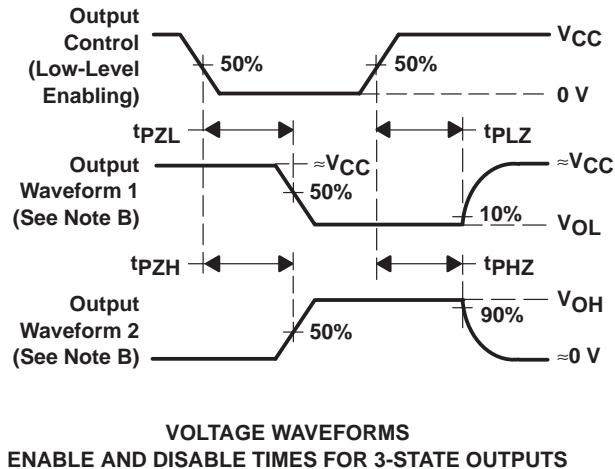
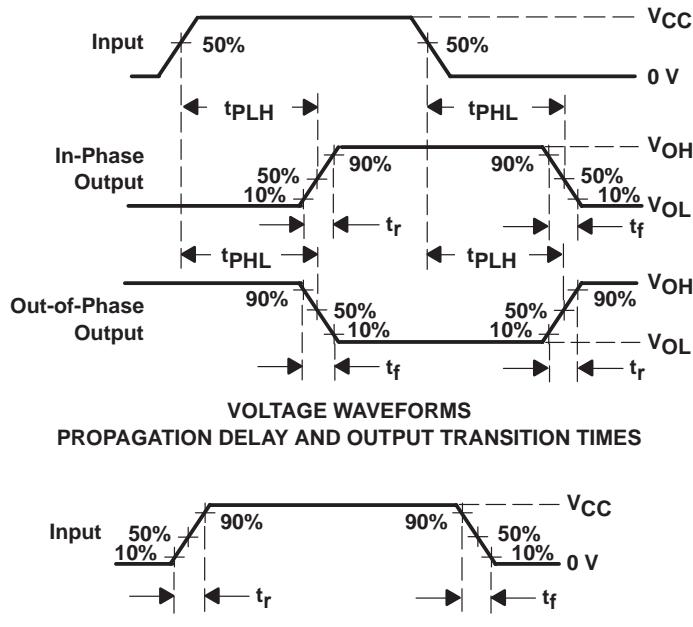


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PARAMETER MEASUREMENT INFORMATION



PARAMETER	R _L	C _L	S1	S2
t _{en}	1 kΩ	50 pF or 150 pF	Open	Closed
			Closed	Open
t _{dis}	1 kΩ	50 pF	Open	Closed
			Closed	Open
t _{pd} or t _t	--	50 pF or 150 pF	Open	Open



- NOTES:
- A. C_L includes probe and test-fixture capacitance.
 - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 - C. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: PRR ≤ 1 MHz, Z_O = 50 Ω, t_r = 6 ns, t_f = 6 ns.
 - D. The outputs are measured one at a time with one input transition per measurement.
 - E. tPLH and tPHL are the same as t_{pd}.
 - F. tPLZ and tPHZ are the same as t_{dis}.
 - G. tPZL and tPZH are the same as t_{en}.

Figure 1. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
85002012A	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
8500201EA	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
JM38510/65708BEA	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
JM38510/65708BFA	ACTIVE	CFP	W	16	1	TBD	Call TI	Level-NC-NC-NC
SN54HC367J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
SN74HC367D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367DT	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367DTE4	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367N	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC367NE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC367NSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367NSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PW	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PWE4	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PWR	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PWRE4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PWT	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC367PWTE4	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SNJ54HC367FK	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
SNJ54HC367J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(³) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

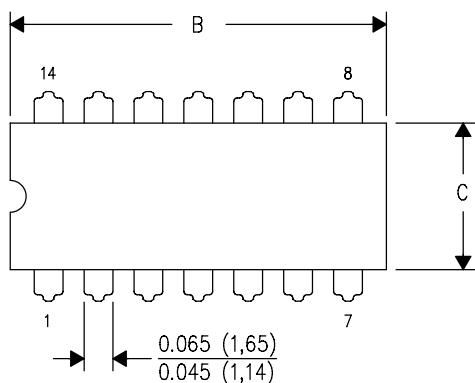
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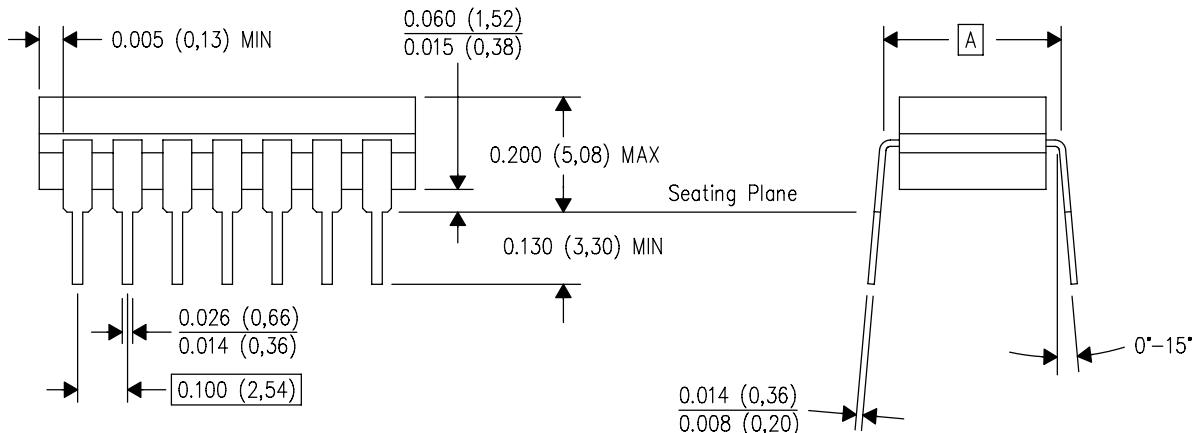
J (R-GDIP-T**)

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



PINS **\nDIM	14	16	18	20
A	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC
B MAX	0.785 (19,94)	.840 (21,34)	0.960 (24,38)	1.060 (26,92)
B MIN	—	—	—	—
C MAX	0.300 (7,62)	0.300 (7,62)	0.310 (7,87)	0.300 (7,62)
C MIN	0.245 (6,22)	0.245 (6,22)	0.220 (5,59)	0.245 (6,22)

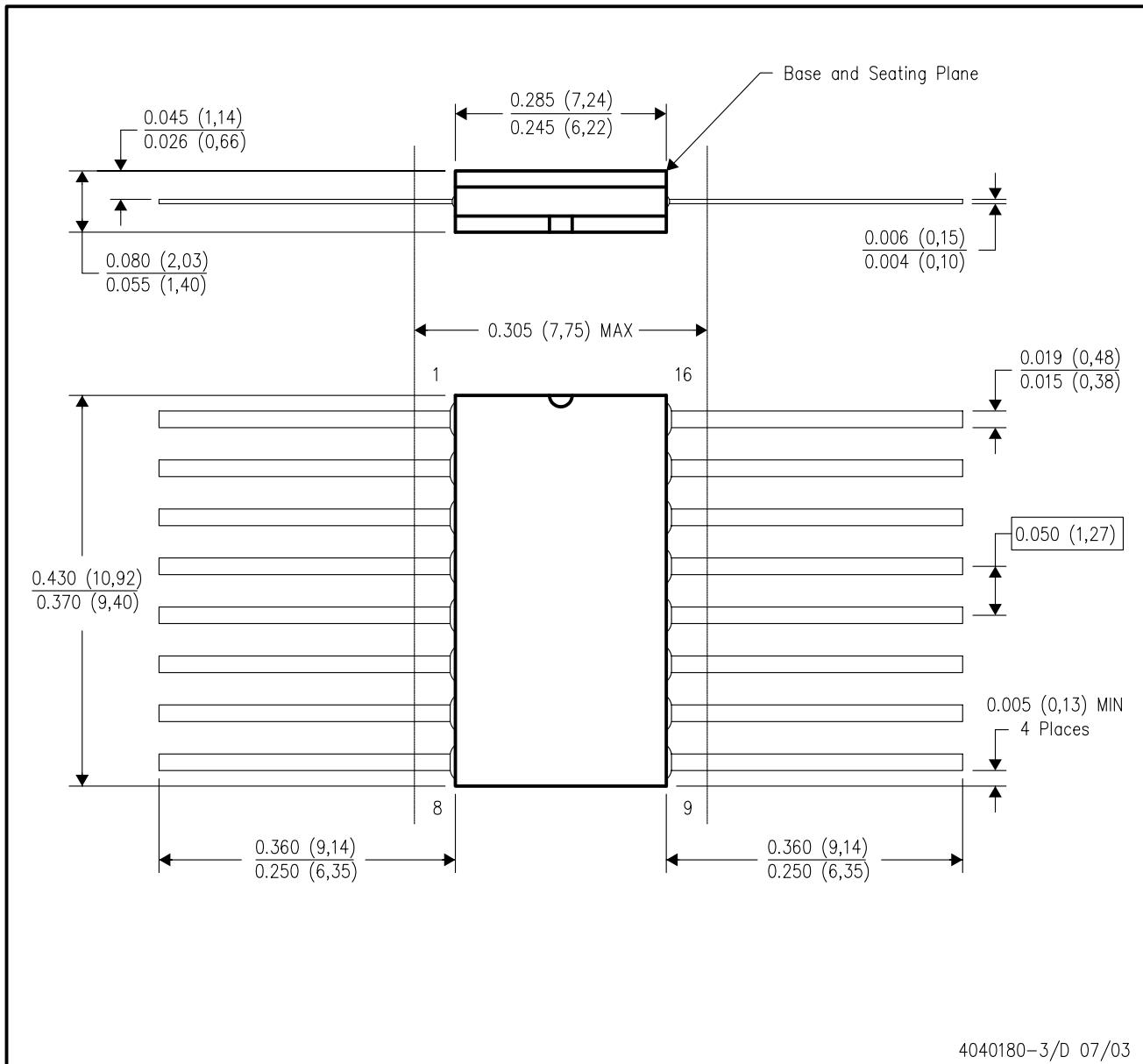


4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. This package is hermetically sealed with a ceramic lid using glass frit.
 - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
 - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

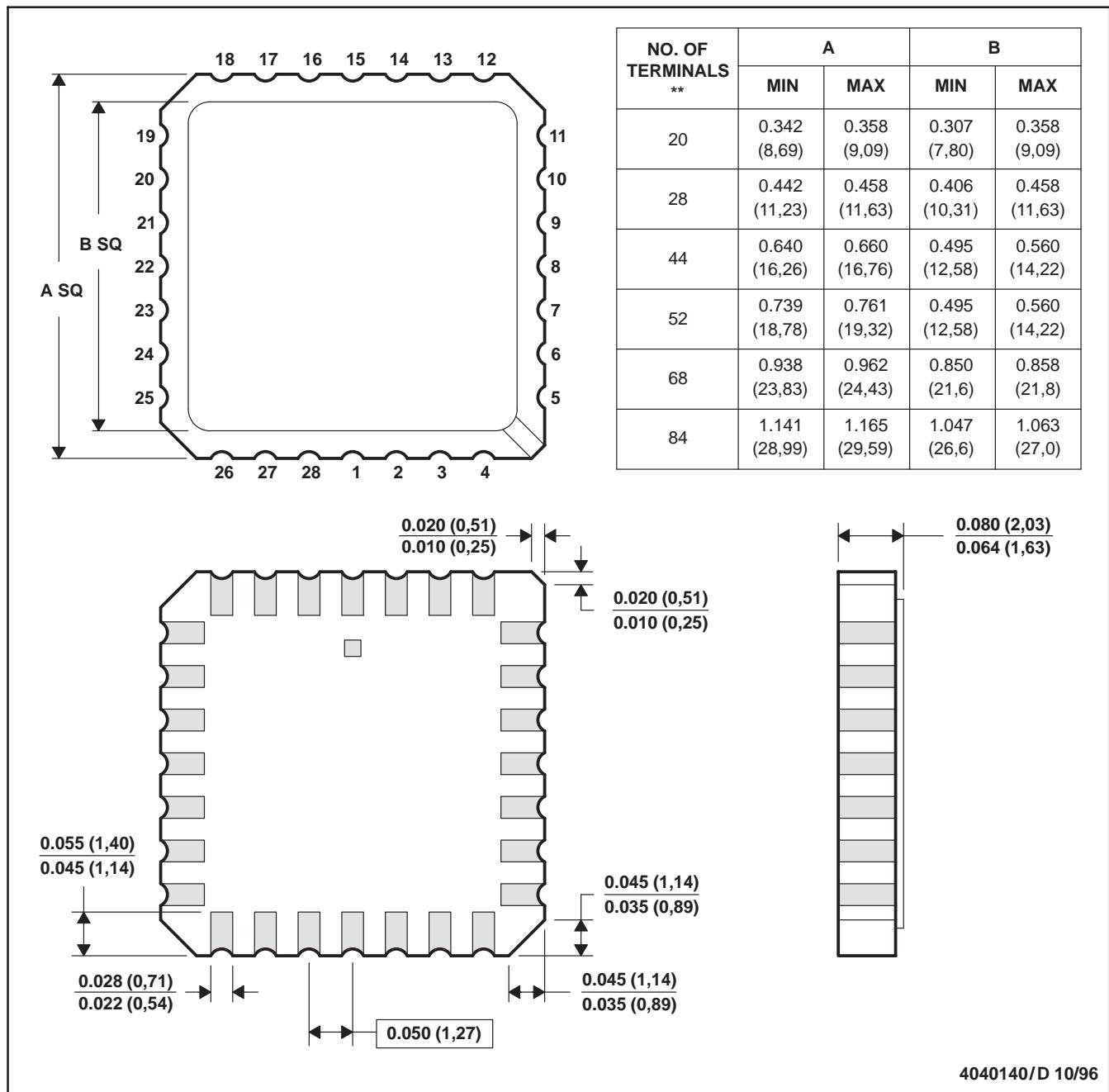
CERAMIC DUAL FLATPACK



FK (S-CQCC-N**)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. This package can be hermetically sealed with a metal lid.

D. The terminals are gold plated.

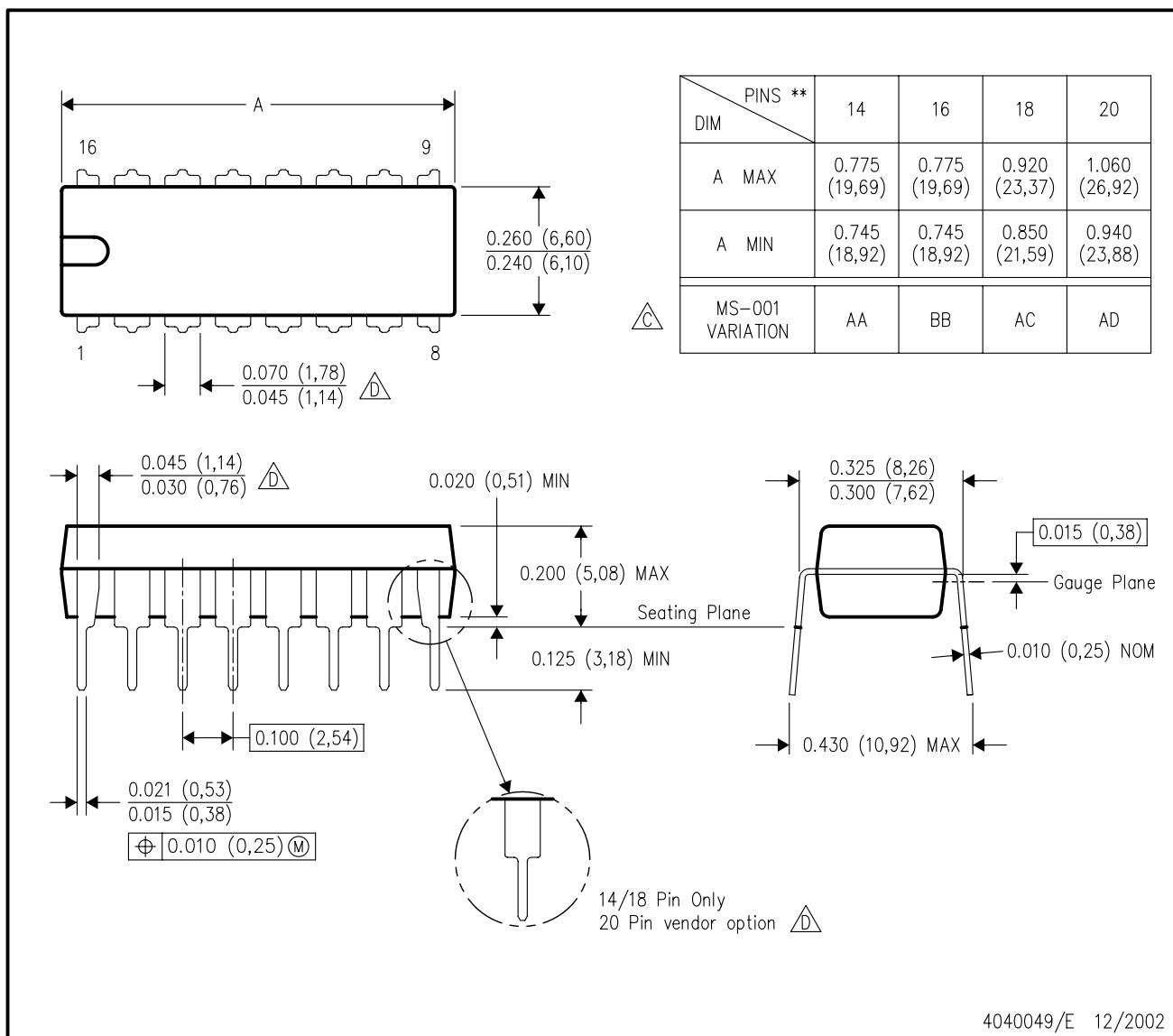
E. Falls within JEDEC MS-004

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N (R-PDIP-T**)

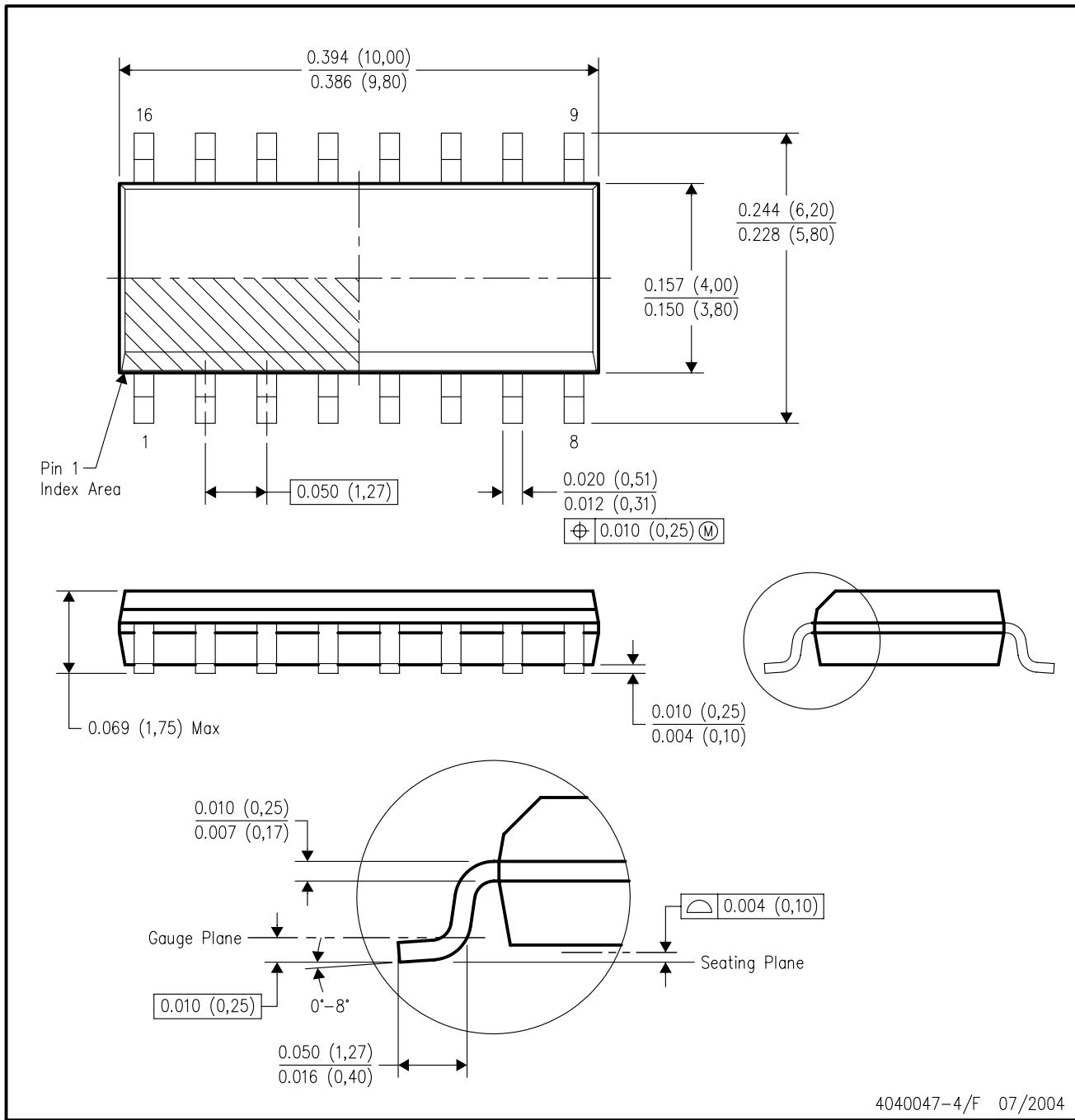
16 PINS SHOWN

PLASTIC DUAL-IN-LINE PACKAGE



D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



4040047-4/F 07/2004

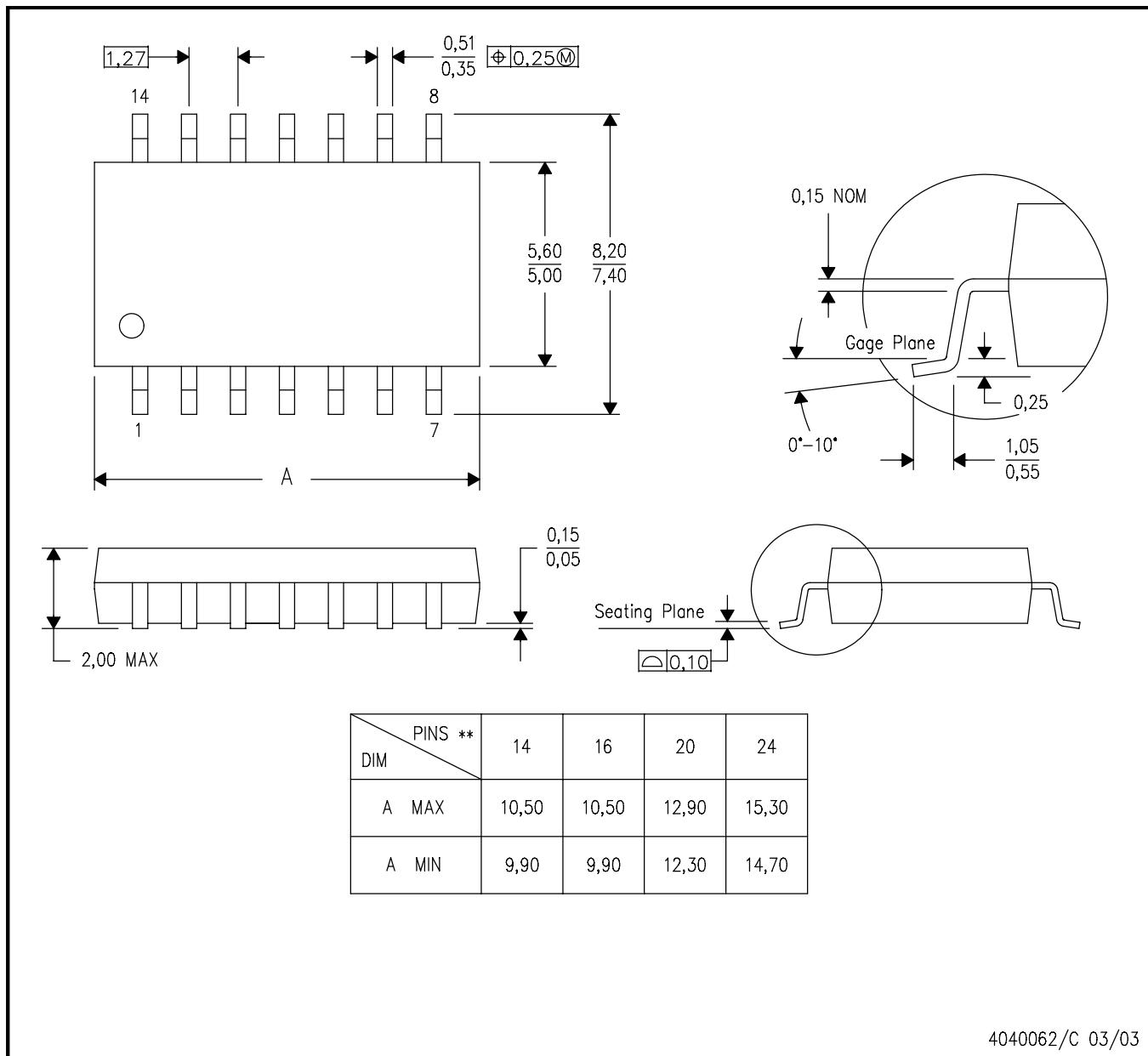
- NOTES:
- All linear dimensions are in inches (millimeters).
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 - Falls within JEDEC MS-012 variation AC.

MECHANICAL DATA

NS (R-PDSO-G)**

14-PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE

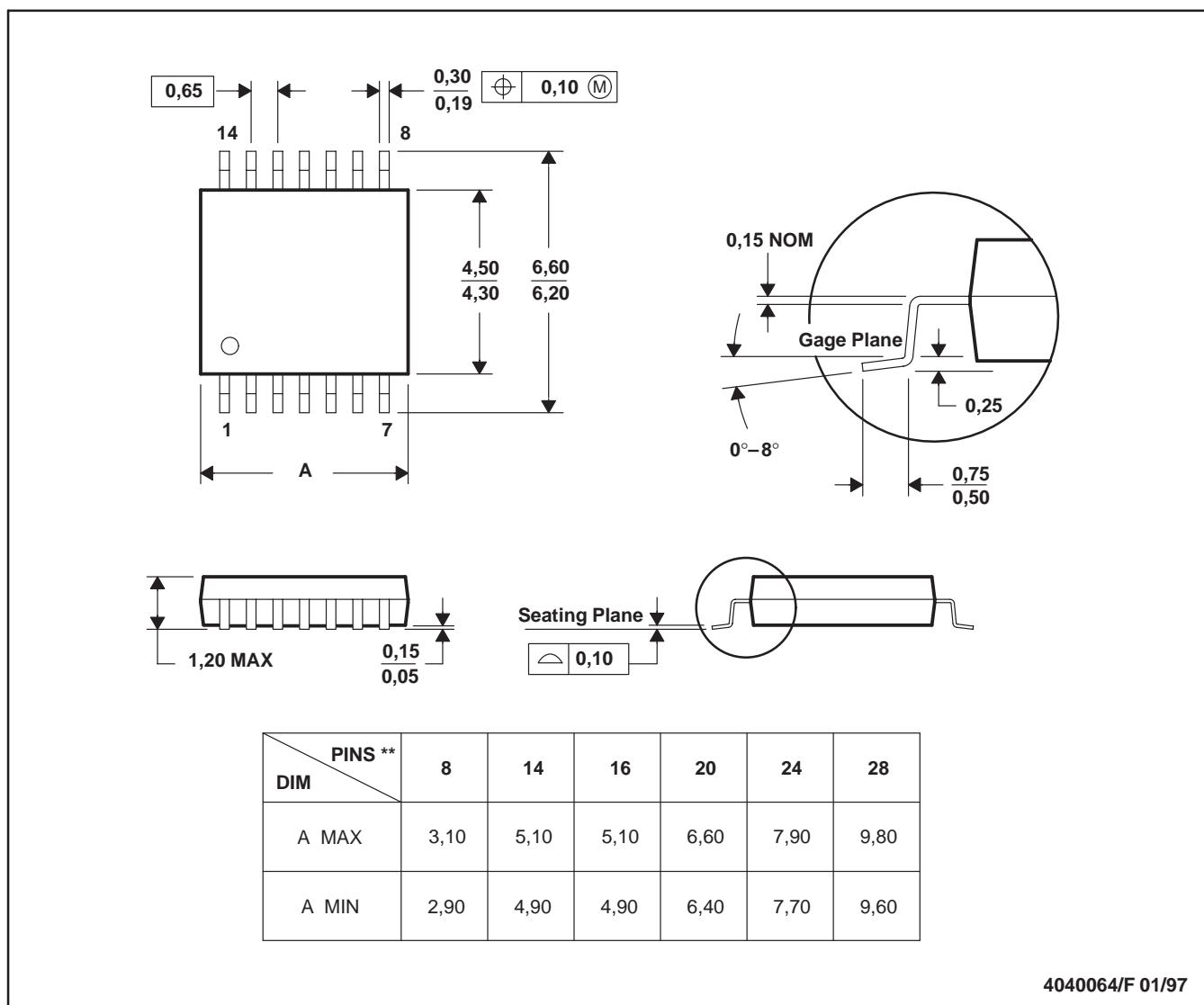


- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 - Falls within JEDEC MO-153

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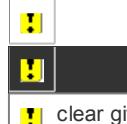
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SN74HC367, Status: ACTIVE

Hex Buffers And Line Drivers With 3-State Outputs

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 Features	 Samples	 Technical Documents
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 Related Products	 Inventory	 Simulation Models
 Tools & Software	 Symbols/Footprints	 Reference Designs

Refine Your Selection

- Logic: Non-Inverting Buffers and Drivers

Support

- KnowledgeBase
- Contact Technical Support
- TI Cross Reference
- Training
- Part Marking Lookup
- Part Number Nomenclature

Datasheet

 [Download Datasheet](#)**SN54HC367, SN74HC367 (Rev. D)** (sn74hc367.pdf, 515 KB)26 Sep 2003 [Download](#)

	SN54HC367	SN74HC367
Voltage Nodes(V)	6, 5, 2	6, 5, 2
Vcc range(V)	2.0 to 6.0	2.0 to 6.0
Logic	True	True
Input Level	CMOS	CMOS
Output Level	CMOS	CMOS
Output Drive(mA)	-6/6	
No. of Outputs	6	6
tpd max(ns)		20
Static Current		0.08
	Samples	Samples
	Inventory	Inventory

Product Information

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Wide Operating Voltage Range of 2 V to 6 V
High-Current 3-State Outputs Drive Bus Lines, Buffer Memory Address Registers, or Drive Up To 15 LSTTL Loads
True Outputs
Low Power Consumption, 80- μ A Max I_{CC}
Typical t_{pd} = 10 ns
±6-mA Output Drive at 5 V
Low Input Current of 1 μ A Max

Description

These hex buffers and line drivers are designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. The 'HC367 devices are organized as dual 4-line and 2-line buffers/drivers with active-low output-enable (OE\ and 2OE\) inputs. When OE\ is low, the device passes noninverted data from the A inputs to the Y outputs. When OE\ is high, the outputs are in the high-impedance state.

Pricing/Packaging/CAD Design Tools/Samples

			Price	Packaging			CAD Design Tools	Samples
Device	Status	Temp (°C)	Budget Price (\$US) QTY	Industry Standard (TI Pkg) Pins	Top Side Marking	Standard Pack Quantity	Footprints	Samples
SN74HC367D	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 16	View	40	<input type="checkbox"/>	Purchase Samples
SN74HC367DE4	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 16	View	40	<input type="checkbox"/>	Purchase Samples
SN74HC367DR	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 16	View	2500	<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC367DRE4	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 16	View	2500	<input type="checkbox"/>	Request Free Samples
SN74HC367DT	ACTIVE	-40 to 85	0.36 1KU	SOIC (D) 16	View	250	<input type="checkbox"/>	Purchase Samples
SN74HC367DTE4	ACTIVE	-40 to 85	0.36 1KU	SOIC (D) 16	View	250	<input type="checkbox"/>	Purchase Samples
SN74HC367N	ACTIVE	-40 to 85	0.35 1KU	PDIP (N) 16	View	25	<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC367NE4	ACTIVE	-40 to 85	0.35 1KU	PDIP (N) 16	View	25	<input type="checkbox"/>	Request Free Samples
SN74HC367NSR	ACTIVE	-40 to 85	0.29 1KU	SO (NS) 16	View	2000	<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC367NSRE4	ACTIVE	-40 to 85	0.29 1KU	SO (NS) 16	View	2000	<input type="checkbox"/>	Purchase Samples
SN74HC367PW	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 16	View	90	<input type="checkbox"/>	Purchase Samples
SN74HC367PWE4	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 16	View	90	<input type="checkbox"/>	Purchase Samples
SN74HC367PWR	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 16	View	2000	<input type="checkbox"/>	Purchase Samples
SN74HC367PWRE4	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 16	View	2000	<input type="checkbox"/>	Purchase Samples
SN74HC367PWT	ACTIVE	-40 to 85	0.36 1KU	TSSOP (PW) 16	View	250	<input type="checkbox"/>	Purchase Samples
SN74HC367PWTE4	ACTIVE	-40 to 85	0.36 1KU	TSSOP (PW) 16	View	250	<input type="checkbox"/>	Purchase Samples

Inventory

			TI Inventory Status		Reported Distributor Inventory										
			As of 9:11 AM GMT, 29 Nov 2005		As of 9:11 AM GMT, 29 Nov 2005										
			In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase						
SN74HC367D	0*	>10k 21 Dec	10 Weeks	Americas	Avnet	423	<input type="checkbox"/>								
							DigiKey	>1k	<input type="checkbox"/>						
							Abacus Polar	80	<input type="checkbox"/>						
				Europe	EBV Elektronik	280	<input type="checkbox"/>								
							Spoerle	>1k	<input type="checkbox"/>						
							<input type="checkbox"/>								
SN74HC367DE4			As of 9:11 AM GMT, 29 Nov 2005		As of 9:11 AM GMT, 29 Nov 2005										
			In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase						
	0*	>10k 21 Dec	10 Weeks	None Reported		None Reported	<input type="checkbox"/>								
				View Distributors			<input type="checkbox"/>								
SN74HC367DR			As of 9:11 AM GMT, 29 Nov 2005		As of 9:11 AM GMT, 29 Nov 2005										
			In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase						
	3834*	>10k 22 Dec	4 Weeks	Americas	DigiKey	>1k	<input type="checkbox"/>								
							Abacus Polar	<input type="checkbox"/>							
								<input type="checkbox"/>							

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[Choose a Region](#)



SN74HC367DRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	3834*	>10k 22 Dec	4 Weeks	None Reported View Distributors			
SN74HC367DT	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 20 Dec	10 Weeks	None Reported View Distributors			
SN74HC367DTE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 20 Dec	10 Weeks	None Reported View Distributors			
SN74HC367N	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 16 Jan	10 Weeks	Americas	DigiKey	69	
				Europe	Abacus Polar	>1k	
					Avnet-SILICA	175	
					Spoerle	>1k	
SN74HC367NE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 16 Jan	10 Weeks	None Reported View Distributors			
SN74HC367NSR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	536 3 Jan	10 Weeks	Americas	DigiKey	>1k	
		97 9 Jan					
		790 16 Jan					
		683 23 Jan					
		614 30 Jan					
SN74HC367NSRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	536 3 Jan	10 Weeks	None Reported View Distributors			
		97 9 Jan					
		790 16 Jan					
		683 23 Jan					
		614 30 Jan					
SN74HC367PW	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	540*	>10k 3 Apr	12 Weeks	None Reported View Distributors			
SN74HC367PWE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	540*	>10k 3 Apr	12 Weeks	None Reported View Distributors			
SN74HC367PWR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 12 Jan	12 Weeks	None Reported View Distributors			

SN74HC367PWRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 12 Jan	12 Weeks	None Reported	View Distributors		
SN74HC367PWT	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	View Distributors		
SN74HC367PWTE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	View Distributors		

* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your [TI Authorized Distributor](#), including those [listed above](#), for real time stock information.

** Lead time information is not available at this time. However, our information is updated daily so please check back with us soon. Please contact your preferred [TI Authorized Distributor](#) for additional information.

Quality & Lead (Pb)-Free Data

	Product Content				MTBF/FIT Rate
Device	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details
SN74HC367D <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367DE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367DR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367DRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367DT <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367DTE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367N <input type="checkbox"/>	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	View	View
SN74HC367NE4 <input type="checkbox"/>	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	View	View
SN74HC367NSR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367NSRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PW <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PWE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PWR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PWRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PWT <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HC367PWTE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View

* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

If the information you are requesting is not available online at this time, contact one of our [Product Information Centers](#) regarding the availability of this information.

Technical Documents

Datasheets	Keep track of what's new
SN54HC367, SN74HC367 (Rev. D) (sn74hc367.pdf, 515 KB)	
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