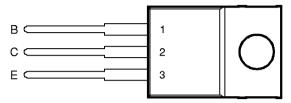
- Designed for Complementary Use with the BD244 Series
- 65 W at 25°C Case Temperature
- 6 A Continuous Collector Current
- 10 A Peak Collector Current
- Customer-Specified Selections Available

# TO-220 PACKAGE (TOP VIEW)



Pin 2 is in electrical contact with the mounting base.

MDTRACA

### absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	BD243		55	
Collector-emitter voltage ( $R_{BE} = 100 \Omega$ )	BD243A	\ \ <u>\</u>	70	<sub>v</sub>
Collector-entitler voltage (n <sub>BE</sub> = 100 sz)	BD243B	V <sub>CER</sub>	90	'
	BD243C		115	
	BD243		45	
Collector-emitter voltage (I <sub>C</sub> = 30 mA)	BD243A	\ \ <u>\</u>	60	l v
	BD243B	V <sub>CEO</sub>	80	'
	BD243C		100	
Emitter-base voltage		V <sub>EBO</sub>	5	V
Continuous collector current		lo lo	6	A
Peak collector current (see Note 1)	I <sub>CM</sub>	10	Α	
Continuous base current			3	■ A
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			65	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			2	W
Unclamped inductive load energy (see Note 4)			62.5	mJ
Operating junction temperature range			-65 to +150	°C
Storage temperature range			-65 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds	T <sub>L</sub>	250	°C	

NOTES: 1. This value applies for  $t_p \le 0.3$  ms, duty cycle  $\le 10\%$ .

- 2. Derate linearly to 150°C case temperature at the rate of 0.52 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH,  $I_{B(on)}$  = 0.4 A,  $R_{BE}$  = 100  $\Omega$ ,  $V_{BE(off)}$  = 0,  $R_S$  = 0.1  $\Omega$ ,  $V_{CC}$  = 20 V.

# BD243, BD243A, BD243B, BD243C NPN SILICON POWER TRANSISTORS

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## electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> = 30 mA (see Note 5)	I <sub>B</sub> = 0	BD243 BD243A BD243B BD243C	45 60 80 100			٧
I <sub>CES</sub>	Collector-emitter cut-off current	$V_{CE} = 55 \text{ V}$ $V_{CE} = 70 \text{ V}$ $V_{CE} = 90 \text{ V}$ $V_{CE} = 115 \text{ V}$	$V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$	BD243 BD243A BD243B BD243C			0.4 0.4 0.4 0.4	mA
I <sub>CEO</sub>	Collector cut-off current	V <sub>CE</sub> = 30 V V <sub>CE</sub> = 60 V	I <sub>B</sub> = 0 I <sub>B</sub> = 0	BD243/243A BD243B/243C			0.7 0.7	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> = 5 V	I <sub>C</sub> = 0				1	mA
h <sub>FE</sub>	Forward current transfer ratio	V <sub>CE</sub> = 4 V V <sub>CE</sub> = 4 V	$I_{\rm C} = 0.3  \text{A}$ $I_{\rm C} = 3  \text{A}$	(see Notes 5 and 6)	30 15			
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>B</sub> = 1 A	I <sub>C</sub> = 6 A	(see Notes 5 and 6)			1.5	٧
V <sub>BE</sub>	Base-emitter voltage	V <sub>CE</sub> = 4 V	I <sub>C</sub> = 6 A	(see Notes 5 and 6)			2	٧
h <sub>fe</sub>	Small signal forward current transfer ratio	V <sub>CE</sub> = 10 V	I <sub>C</sub> = 0.5 A	f = 1 kHz	20			
h <sub>fe</sub>	Small signal forward current transfer ratio	V <sub>CE</sub> = 10 V	I <sub>C</sub> = 0.5 A	f = 1 MHz	3			

NOTES: 5. These parameters must be measured using pulse techniques,  $t_p = 300 \,\mu s$ , duty cycle  $\leq 2\%$ .

### thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.92	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

## resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †		MIN	TYP	MAX	UNIT	
t <sub>on</sub>	Turn-on time	I <sub>C</sub> = 1 A	$I_{B(on)} = 0.1 A$	I <sub>B(off)</sub> = -0.1 A		0.3		μs
t <sub>off</sub>	Turn-off time	$V_{BE(off)} = -3.7 \text{ V}$	$R_L = 20 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		1		μs

<sup>&</sup>lt;sup>†</sup> Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

<sup>6.</sup> These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

### TYPICAL CHARACTERISTICS

# TYPICAL DC CURRENT GAIN vs COLLECTOR CURRENT TCS633AH $T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$ $T_{c} = 300 \,\mu s, \, duty \, cycle < 2\%$ $T_{c} = 100 \, duty \, cycle < 2\%$ $T_{c} = 100 \, duty \, cycle < 2\%$ $T_{c} = 100 \, duty \, cycle < 2\%$ $T_{c} = 100 \, duty \, cycle < 100 \, duty \, cycle$

Figure 1.

# **COLLECTOR-EMITTER SATURATION VOLTAGE**

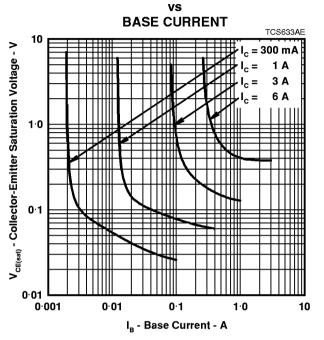


Figure 2.

### **BASE-EMITTER VOLTAGE**

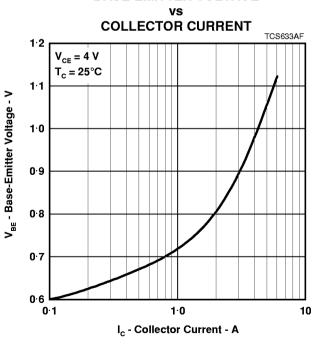
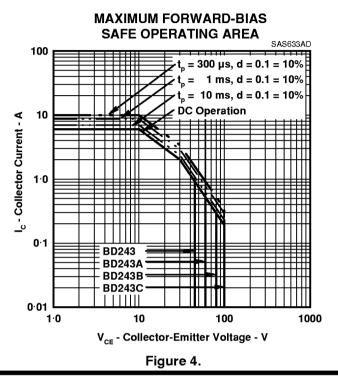


Figure 3.



### **MAXIMUM SAFE OPERATING REGIONS**



### THERMAL INFORMATION

### **MAXIMUM POWER DISSIPATION**

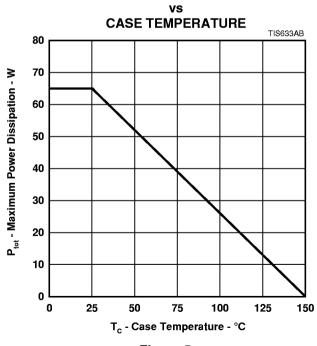


Figure 5.

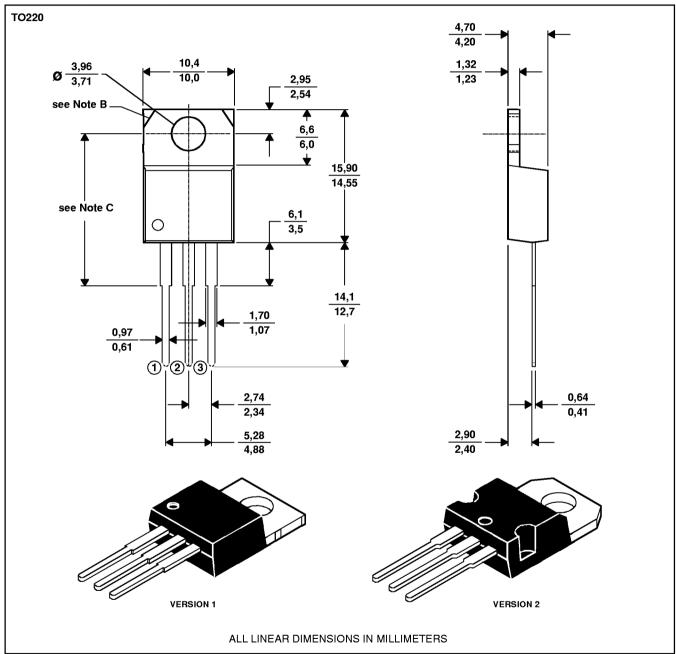
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### **MECHANICAL DATA**

### TO-220

## 3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.
C. Typical fixing hole centre stand off height according to package version.
Version 1, 18.0 mm. Version 2, 17.6 mm.

MDXXBE



# BD243, BD243A, BD243B, BD243C NPN SILICON POWER TRANSISTORS

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